

Part Number: KTIR0221DS

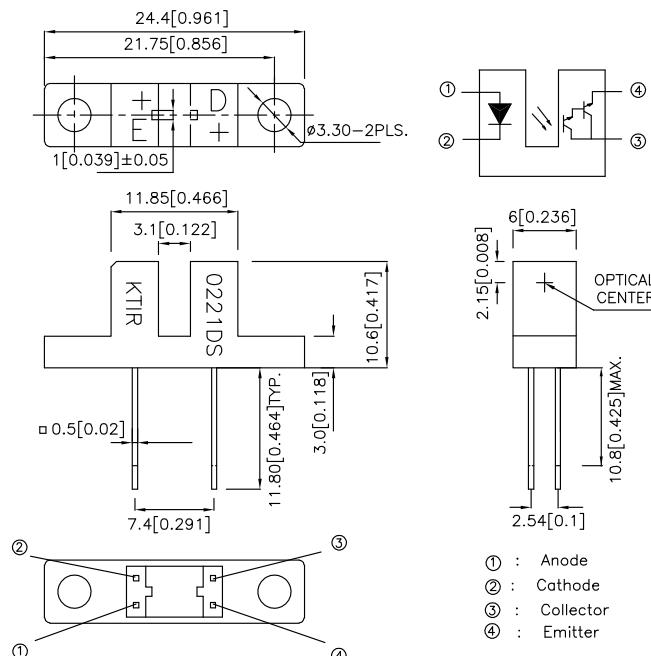
Features

- High sensing accuracy
- High current transfer ratio
- Both-sides mounting type
- RoHS compliant.

Applications

- OA equipment, such as floppy disk drives, printers, facsimiles, etc
- VCRs

Package Dimensions



Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is $\pm 0.25(0.01")$ unless otherwise noted.
3. Lead spacing is measured where the leads emerge from the package.
4. Specifications are subject to change without notice.

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$)

	Parameter	Symbol	Rating	Unit
Input	Forward current	I _F	50	mA
	Reverse voltage	V _R	6	V
	Power dissipation	P _d	75	mW
	Peak Forward Current (Pulse Width $\leq 100\mu\text{s}$, Duty Ratio=1%)	I _{FP}	1	A
Output	Collector-emitter voltage	V _{CEO}	35	V
	Emitter-collector voltage	V _{ECO}	6	V
	Collector current	I _C	40	mA
	Collector power dissipation	P _C	75	mW
Operating temperature		T _{opr}	-25~+85	°C
Storage temperature		T _{stg}	-40~+100	°C
Soldering temperature (1/16 inch from body for 5 seconds)		T _{sol}	260	°C



Electro-optical Characteristics(Ta=25°C)

Parameter		Symbol	Conditions	Min.	Typ.	Max.	Unit
Input	Forward voltage	V _F	I _F =20mA	—	1.2	1.5	V
	Peak forward voltage	V _{FM}	I _{FM} =0.5A	—	2	4	V
	Reverse current	I _R	V _R =5V	—	—	10	μA
Output	Collector dark current	I _{CEO}	V _{CE} =10V, I _F =0mA	—	—	10 ⁻⁶	A
Transfer Characteristics	Current transfer ratio	CTR	V _{CE} =2V, I _F =1mA	—	600	—	%
	Collector-emitter saturation voltage	V _{CE(SAT)}	I _F =2mA, I _C =1mA	—	—	1.0	V
	Response time	tr	V _{CE} =2V, I _C =10mA R _L =100Ω	—	90	400	μSec
	Fall time	tf		—	80	300	μSec

Fig.1 Forward Current vs. Forward Voltage

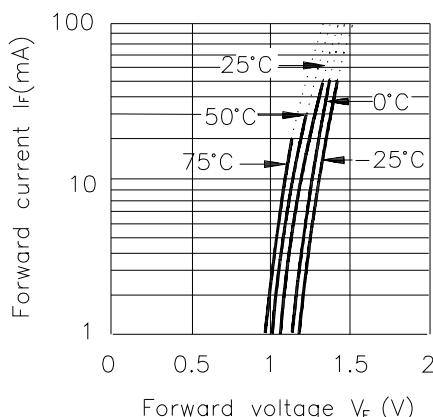


Fig.2 Collector Current vs. Forward Current

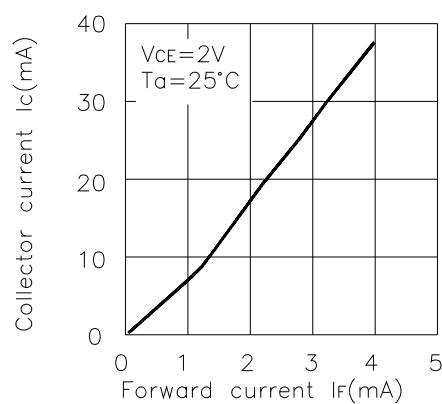


Fig.3 Collector Current vs. Collector-emitter Voltage

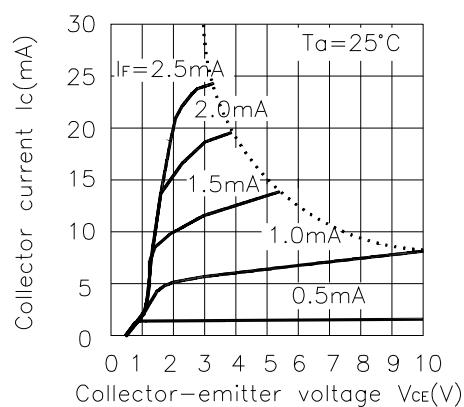


Fig.4 Collector Current vs. Ambient Temperature

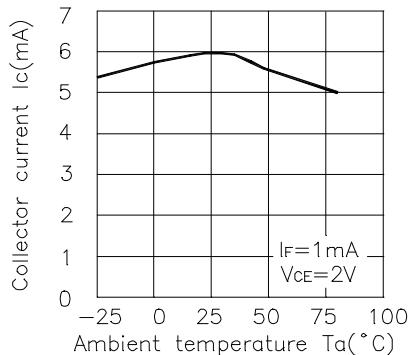


Fig.5 Collector-emitter Saturation Voltage vs. Ambient Temperature

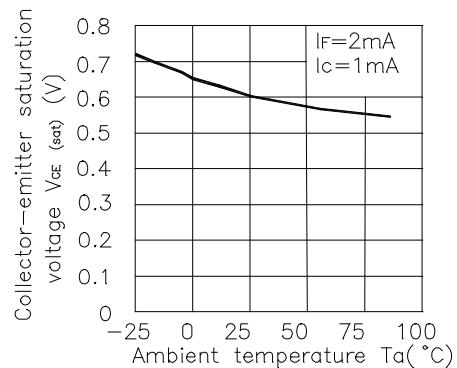


Fig.6 Relative Collector Current vs. Shield Distance(1)

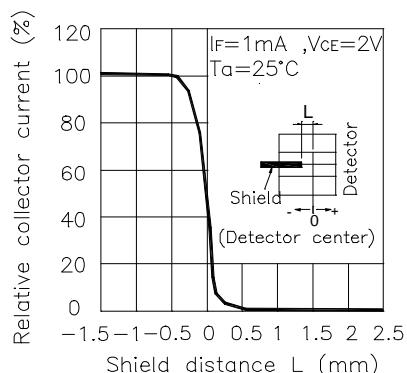


Fig.7 Relative Collector Current vs. Shield Distance(2)

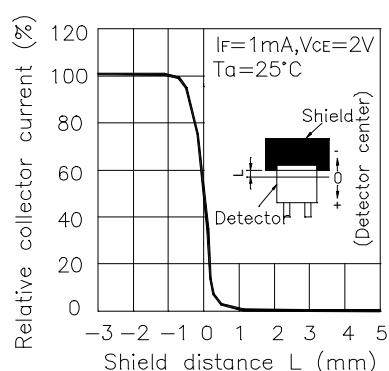
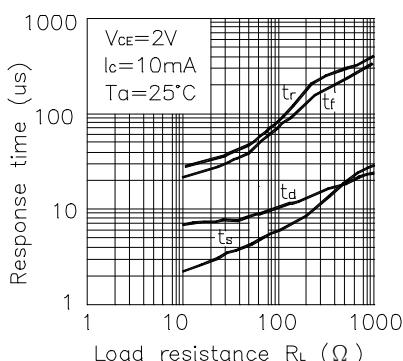


Fig.8 Response Time vs. Load Resistance



Test Circuit for Response Time

