

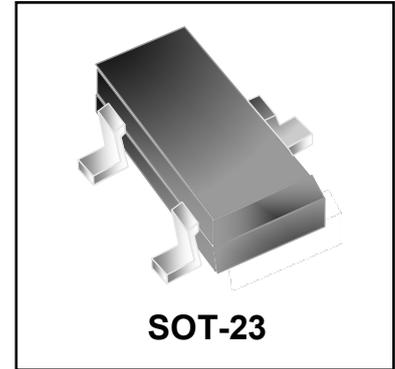
P-Channel Enhancement MOSFET

Features

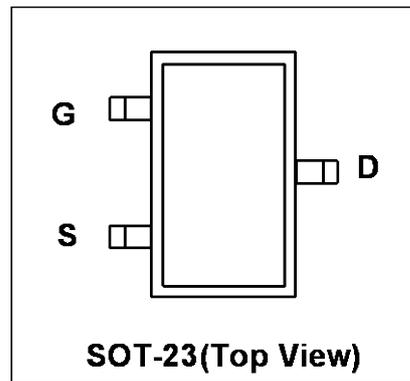
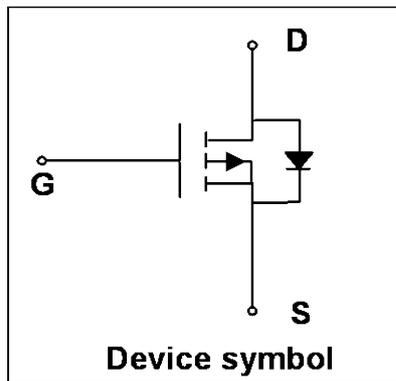
- Way-on Small Single MOSFETs
- $V_{DS} = -30\text{ V}$, $I_D = -4.1\text{ A}$
 $R_{DS(on)} < 60\text{ m}\Omega @ V_{GS} = -10\text{ V}$
 $R_{DS(on)} < 85\text{ m}\Omega @ V_{GS} = -4.5\text{ V}$
- Trench LV MOSFET Technology

Mechanical Characteristics

- SOT-23 Package
- Marking : Making Code
- RoHS Compliant



Schematic & PIN Configuration



Absolute Maximum Rating ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	-4.1	A
Pulsed Drain Current ¹	I_{DM}	-16	A
Power Dissipation	P_D	1.2	W
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Units
Thermal Resistance from Junction to Ambient ²	$R_{\theta JA}$	104	$^\circ\text{C/W}$

Electrical Characteristics (T_J=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V_{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V _{DS} = -30V, V _{GS} = 0V	-	-	-1	μA
Gate-Source Leakage	I_{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Gate-Source Threshold voltage	V_{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1	-1.5	-2.5	V
Drain-Source on-State Resistance ³	R_{DS(on)}	V _{GS} = -10V, I _D = -4.1A	-	42	60	mΩ
		V _{GS} = -4.5V, I _D = -3A	-	54	85	
Dynamic Characteristics⁴						
Input Capacitance	C_{iss}	V _{GS} = 0V , V _{DS} = -15V, f = 1.0MHz	-	530	-	pF
Output Capacitance	C_{oss}		-	70	-	
Reverse Transfer Capacitance	C_{rss}		-	56	-	
Switching Characteristics⁴						
Total Gate Charge	Q_g	V _{GS} = -10V, V _{DS} = -15V, I _D = -4.1A	-	6.8	-	nC
Gate-Source Charge	Q_{gs}		-	1.0	-	
Gate-Drain Charge	Q_{gd}		-	1.4	-	
Turn-on Delay Time	t_{d(on)}	V _{GS} = -10V, V _{DS} = -15V , R _L = 15Ω,R _{GEN} = 2.5Ω	-	14	-	ns
Rise Time	t_r		-	61	-	
Turn-off Delay time	t_{d(off)}		-	19	-	
Fall Time	t_f		-	10	-	
Source-Drain Body Diode Characteristics						
Diode Forward Voltage ³	V_{SD}	I _S = -4.1A, V _{GS} = 0V	-	-	-1.2	V
Continuous Source Current	I_S		-	-	-4.1	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface mounted on FR4 board using 1 square inch pad size, 1oz single-side copper.
3. Pulse Test: Pulse width≤300μs, duty cycle≤2%.
4. Guaranteed by design, not subject to product.

Typical Characteristics

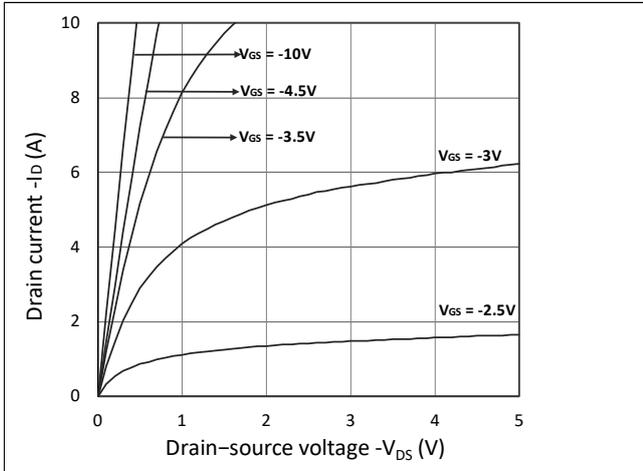


Figure 1. Output Characteristics

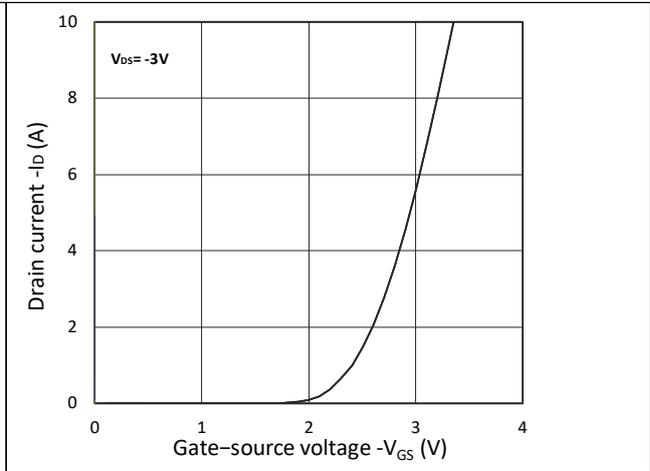


Figure 2. Transfer Characteristics

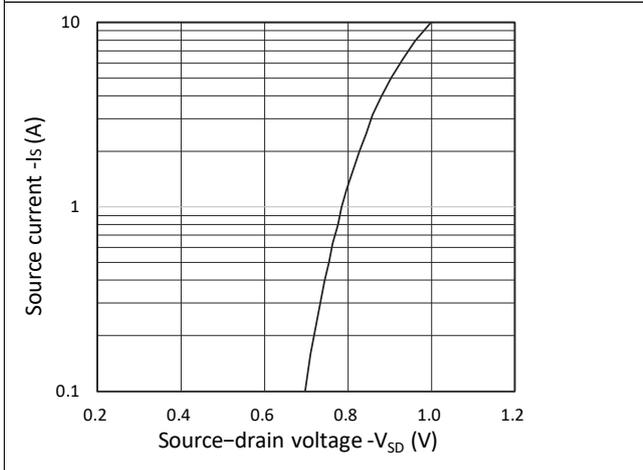


Figure 3. Forward Characteristics of Reverse

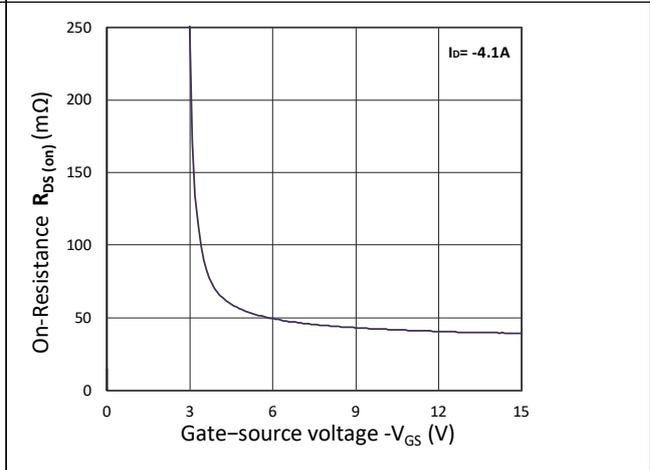


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

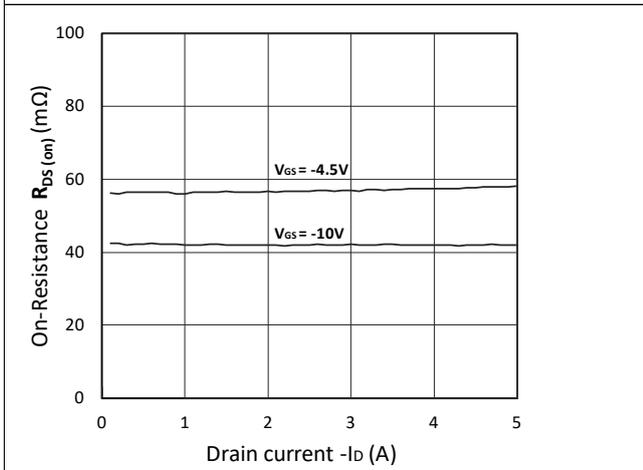


Figure 5. $R_{DS(ON)}$ vs. I_D

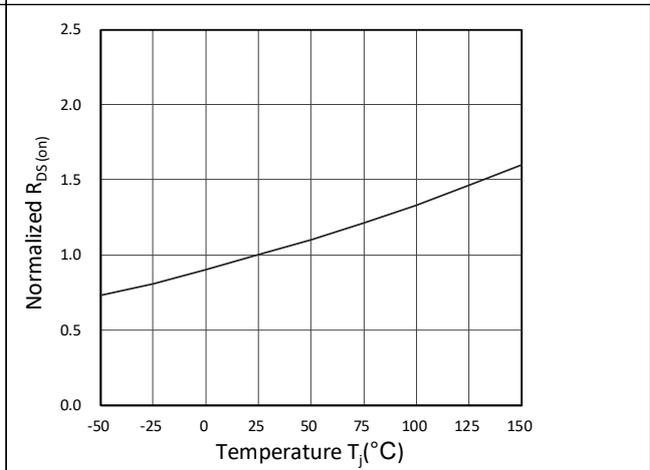


Figure 6. Normalized $R_{DS(ON)}$ vs. Temperature

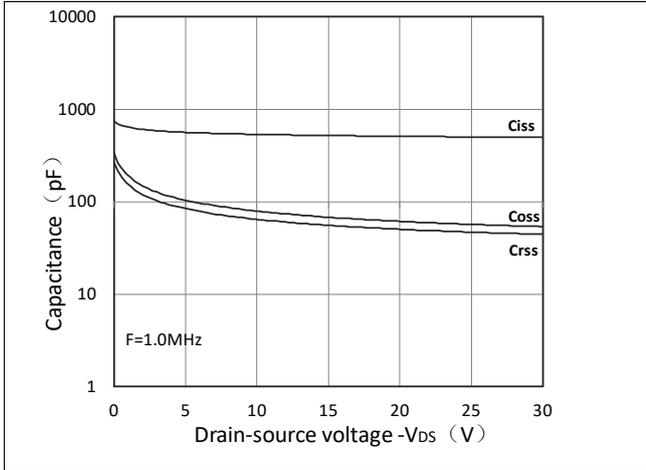


Figure 7. Capacitance Characteristics

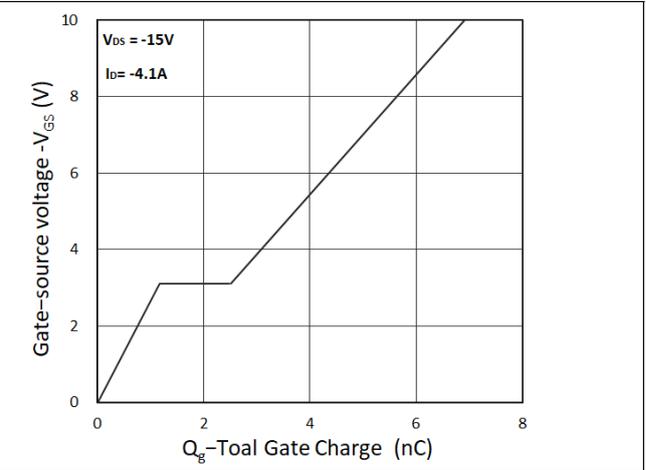
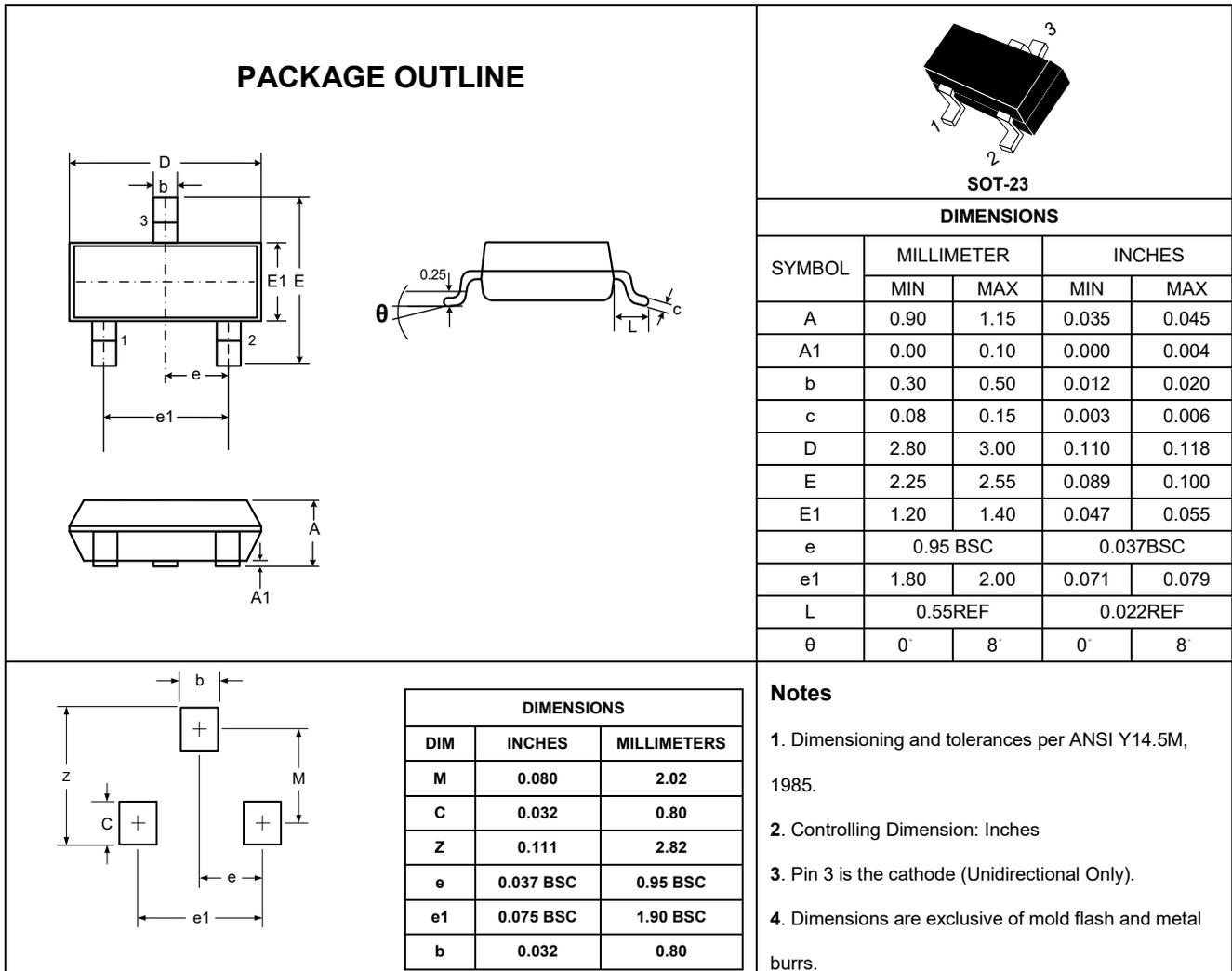


Figure 8. Gate Charge Characteristics

Outline Drawing – SOT-23



Marking Codes

Part Number	WM03P41M
Marking Code	

Package Information

Qty: 3k/Reel

CONTACT INFORMATION

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For additional information, please contact your local Sales Representative.

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*Specifications are subject to change without notice.
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.
Users should verify actual device performance in their specific applications.*