

Precision, CMOS, Rail-to-Rail Input /Output, Wideband Operational Amplifier

PRODUCT DESCRIPTION

The MS8601,MS8602,MS8604 are single,dual,four channel rail-to-rail input/output and single power amplifiers respectively. The device is featured by ultra-low offset voltage and wide signal bandwidth. And it adopts 1.8V to 5V single power supply (± 0.9 V to ± 2.5 V dual power).

The MS8601, MS8602, MS8604 integrates low offset voltage, ultra-low bias current and high speed characteristics, which are suitable for various applications, such as filter,integrator,diode amplifier,shunt sensor and high-impedance sensor. The low offset voltage, low offset voltage drift and noise features, making the device drift approach zero throughout the operating temperature, are beneficial to position and pressure sensor, medical device and strain gauge applications.

The operating temperature range of the MS8601, MS8602 or MS8604 is -40°C to 125°C. The MS8601, MS8602, MS8604 have lead SOT23-5, MSOP8, SOP14/TSSOP14 packages respectively.

FEATURES

- Low Offset Voltage: 4 μ V(TYP)
- Low Offset Drift: 0.05 μ V/ $^{\circ}$ C
- Rail-to-Rail Input/Output
- Single Power Supply: 1.8V to 5.5V
- Voltage Gain: 126dB(TYP) (Operating Voltage 5V)
- Power Supply Rejection Ratio: 123dB (TYP)
- Common-mode Rejection Ratio: 136dB (TYP)
- Ultra-low Input Bias Current: 11pA
- Low Operating Current: Each Channel 0.8mA (TYP)
- Overload Recovery Time: 50 μ s(Operating Voltage 5V)

APPLICATIONS

- Sensor and Medical Device
- Thermocouple Amplifier
- Precise Current Sensor
- Photodiode Amplifier
- Bar Code Scanner
- PA Control and Multi-order Filter

PRODUCT SPECIFICATION

| Part Number | Package | Marking |
|-------------|---------|---------|
| MS8601 | SOT23-5 | 8601 |
| MS8602 | MSOP8 | MS8602 |
| *MS8604 | SOP14 | MS8604 |
| *MS8604T | TSSOP14 | MS8604T |

*The package is not available temporarily. If necessary, please contact Hangzhou Ruimeng Sales Department Center.



SOT23-5



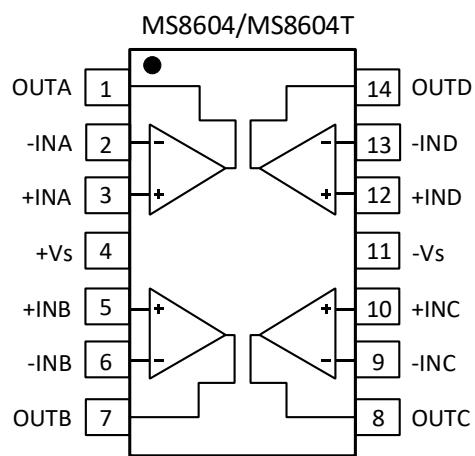
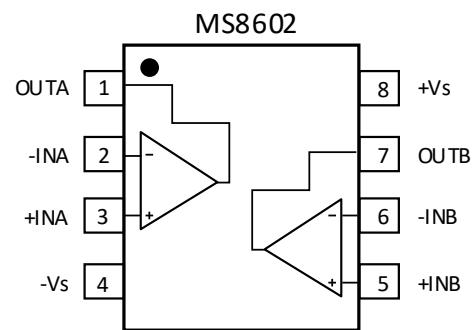
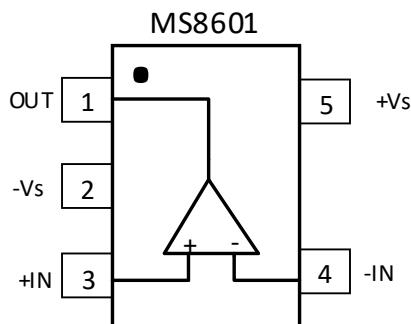
MSOP8



SOP14



TSSOP14

PIN CONFIGURATION


PIN DESCRIPTION

| Pin | Name | Type | Description |
|-----------------------|------|------|----------------------------|
| MS8601 | | | |
| 1 | OUT | O | Channel Output |
| 2 | -Vs | - | Negative Power Supply |
| 3 | +IN | I | Positive Input |
| 4 | -IN | I | Negative Input |
| 5 | +Vs | - | Positive Power Supply |
| MS8602 | | | |
| 1 | OUTA | O | Channel A Output |
| 2 | -INA | I | Negative Input (Channel A) |
| 3 | +INA | I | Positive Input (Channel A) |
| 4 | -Vs | - | Negative Power Supply |
| 5 | +INB | I | Positive Input (Channel B) |
| 6 | -INB | I | Negative Input (Channel B) |
| 7 | OUTB | O | Channel B Output |
| 8 | +Vs | - | Positive Power Supply |
| MS8604/MS8604T | | | |
| 1 | OUTA | O | Channel A Output |
| 2 | -INA | I | Negative Input (Channel A) |
| 3 | +INA | I | Positive Input (Channel A) |
| 4 | +Vs | - | Positive Power Supply |
| 5 | +INB | I | Positive Input (Channel B) |
| 6 | -INB | I | Negative Input (Channel B) |
| 7 | OUTB | O | Channel B Output |
| 8 | OUTC | O | Channel C Output |
| 9 | -INC | I | Negative Input (Channel C) |
| 10 | +INC | I | Positive Input (Channel C) |
| 11 | -Vs | - | Negative Power Supply |
| 12 | +IND | I | Positive Input (Channel D) |
| 13 | -IND | I | Negative Input (Channel D) |
| 14 | OUTD | O | Channel D Output |

ABSOLUTE MAXIMUM RATINGS

Any exceeding absolute maximum rating application causes permanent damage to device. Because long-time absolute operation state affects device reliability. Absolute ratings just conclude from a series of extreme tests. It doesn't represent chip can operate normally in these extreme conditions.

| Parameter | Symbol | Ratings | Unit |
|-----------------------------------|------------------|---------------------------------------------|------|
| Power Supply | V _s | 6 | V |
| Input Voltage | V _I | -V _s -0.3 ~ +V _s +0.3 | V |
| Differential Input Voltage | | ±6 | V |
| Junction Temperature | | -65 ~ 150 | °C |
| Operating Temperature | T _A | -40 ~ 125 | °C |
| Storage Temperature | T _{stg} | -60 ~ 150 | °C |
| Lead Temperature (Soldering, 10s) | | 260 | °C |
| ESD | HBM | 4000 | V |
| | MM | 200 | |

ELECTRICAL CHARACTERISTICS (5V)

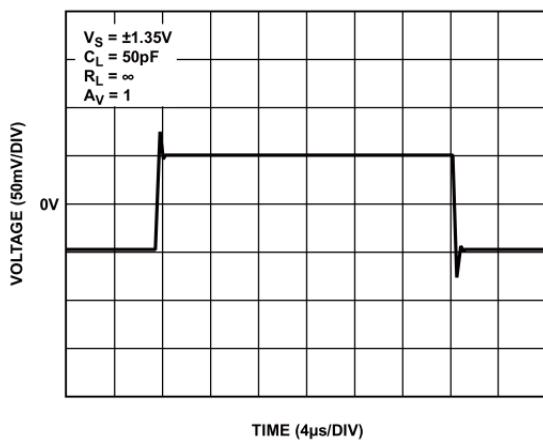
Unless otherwise noted, $+Vs=5V$, $-Vs =0V$, $Vcm=2.5V$, $T_A=25^{\circ}C$.

| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|--------------------------------|----------------------------|---------------------------------------------------------------------|------|-------|------|------------------|
| Input Characteristics | | | | | | |
| Input Offset Voltage | V_{OS} | | | 4 | 8 | μV |
| | | $-40^{\circ}C \leq T_A \leq +125^{\circ}C$ | | | 10 | |
| Input Bias Current | I_B | | | 30 | 100 | pA |
| | | | | 100 | 300 | pA |
| | | $-40^{\circ}C \leq T_A \leq +125^{\circ}C$ | | | 1.5 | nA |
| Input Offset Current | I_{OS} | $-40^{\circ}C \leq T_A \leq +125^{\circ}C$ | | 40 | 250 | pA |
| Input Voltage | | | 0 | | 5 | V |
| Common-mode Rejection Ratio | $CMRR$ | $VCM = 0V$ to $5V$ | 120 | 140 | | dB |
| | | $-40^{\circ}C \leq T_A \leq +125^{\circ}C$ | 115 | 130 | | |
| Large Signal Gain | A_{VO} | $RL = 10k\Omega$, $V_o = 0.3V$ to $4.7V$ | 127 | 145 | | dB |
| | | $-40^{\circ}C \leq T_A \leq +125^{\circ}C$ | 120 | 135 | | |
| Input Offset Voltage Drift | $\Delta V_{OS}/\Delta T_A$ | $-40^{\circ}C \leq T_A \leq +125^{\circ}C$ | | 0.03 | 0.05 | $\mu V/{\circ}C$ |
| Output Characteristics | | | | | | |
| Output High Voltage | V_{OH} | $RL = 100k\Omega$ to $-Vs$ | 4.99 | 4.996 | | V |
| | | $RL = 10k\Omega$ to $-Vs$ | 4.99 | 4.995 | | V |
| Output Low Voltage | V_{OL} | $RL = 100k\Omega$ to $+Vs$ | | 1 | 5 | mV |
| | | $-40^{\circ}C \leq T_A \leq +125^{\circ}C$ | | 2 | 5 | |
| | | $RL = 10k\Omega$ to $+Vs$ | | 10 | 20 | mV |
| | | $-40^{\circ}C \leq T_A \leq +125^{\circ}C$ | | 15 | 20 | |
| Short-circuit Current | I_{SC} | $V_o = 2.5V$, $RL = 10\Omega$ to GND | 25 | 50 | | mA |
| Output Current | I_O | | | 30 | | mA |
| | | $-40^{\circ}C \leq T_A \leq +125^{\circ}C$ | | 15 | | mA |
| Power Dissipation | | | | | | |
| Power Supply Rejection Ratio | PSRR | $V_s = 1.8V$ to $5.5V$, $-40^{\circ}C \leq T_A \leq +125^{\circ}C$ | 115 | 130 | | dB |
| Quiescent Current/Amplifier | I_Q | $V_o = V_s/2$ | | 0.85 | 1.1 | mA |
| Dynamic Characteristics | | | | | | |
| Gain Bandwidth Product | GBP | $A_v = +100$ | | 3.8 | | MHz |
| Slew Rate | SR | $A_v = +1$, $RL = 10k\Omega$ | | 2.1 | | V/ μ s |
| Overload Recovery Time | | | | 0.05 | | ms |
| Noise Characteristics | | | | | | |
| Voltage Noise | $e_{n P-P}$ | $0.1Hz$ to $10Hz$ | | 0.50 | | μV_{p-p} |
| Voltage Noise Density | e_n | $f = 1kHz$ | | 22 | | nV/\sqrt{Hz} |
| Current Noise Density | i_n | $f = 10Hz$ | | 5 | | fA/\sqrt{Hz} |

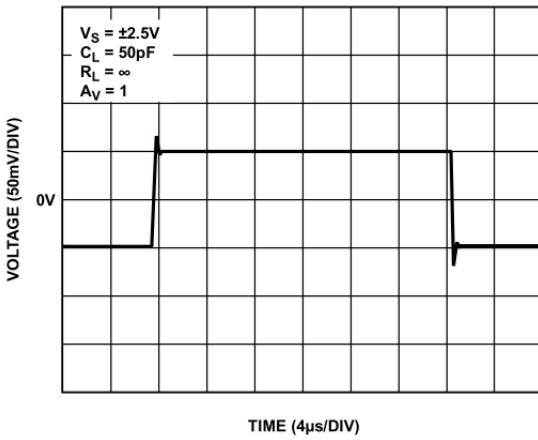
ELECTRICAL CHARACTERISTICS (2.7V)

Unless otherwise noted, $+Vs=2.7V$, $-Vs =0V$, $Vcm=1.35V$, $Vo=+1.35V$, $TA=25^{\circ}C$.

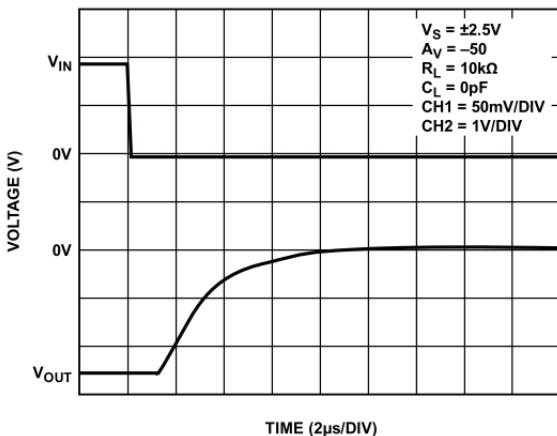
| Parameter | Symbol | Condition | Min | Typ | Max | Unit | | |
|--------------------------------|--------|----------------------------|-----------------------------------------------------------------------|------|-------|------------------|----|--|
| Input Characteristics | | | | | | | | |
| Input Offset Voltage | | V_{os} | | 0.5 | 5 | μV | | |
| | | | $-40^{\circ}C \leq TA \leq +125^{\circ}C$ | | 10 | | | |
| Input Bias Current | MS8604 | I_B | | 30 | 100 | PA | | |
| | | | | 100 | 300 | PA | | |
| | | | $-40^{\circ}C \leq TA \leq +125^{\circ}C$ | 1.0 | 1.5 | nA | | |
| Input Offset Current | | I_{os} | $-40^{\circ}C \leq TA \leq +125^{\circ}C$ | 50 | 250 | pA | | |
| Input Voltage | | | | 0 | 2.7 | V | | |
| Common-mode Rejection Ratio | | CMRR | $V_{CM} = 0V$ to $2.7V$ | 115 | 130 | dB | | |
| | | | $-40^{\circ}C \leq TA \leq +125^{\circ}C$ | 110 | 120 | | | |
| Large Signal Gain | | A_{vo} | $RL = 10k\Omega$, $Vo = 0.3V$ to $2.4V$ | 110 | 140 | dB | | |
| | | | $-40^{\circ}C \leq TA \leq +125^{\circ}C$ | 105 | 130 | | | |
| Input Offset Voltage Drift | | $\Delta V_{os}/\Delta T_A$ | $-40^{\circ}C \leq TA \leq +125^{\circ}C$ | 0.03 | 0.05 | $\mu V/{\circ}C$ | | |
| Output Characteristics | | | | | | | | |
| Output High Voltage | | V_{OH} | $RL = 100k\Omega$ to $-Vs$ | 2.68 | 2.695 | V | | |
| | | | $RL = 10k\Omega$ to $-Vs$ | 2.67 | 2.68 | | | |
| Output Low Voltage | | V_{OL} | $RL = 100k\Omega$ to $+Vs$ | | 1 | 5 | mV | |
| | | | $-40^{\circ}C \leq TA \leq +125^{\circ}C$ | | 2 | 5 | | |
| | | | $RL = 10k\Omega$ to $+Vs$ | | 10 | 20 | mV | |
| | | | $-40^{\circ}C \leq TA \leq +125^{\circ}C$ | | 15 | 20 | | |
| Short-circuit Current | | I_{sc} | $Vo = 2.5V$, $RL = 10\Omega$ to GND | 10 | 15 | mA | | |
| Output Current | | I_o | | | 10 | mA | | |
| | | | $-40^{\circ}C \leq TA \leq +125^{\circ}C$ | | 5 | mA | | |
| Power Dissipation | | | | | | | | |
| Power Supply Rejection Ratio | | PSRR | $V_s = 1.8V$ to $5.5V$, $-40^{\circ}C \leq TA \leq +125^{\circ}C$ | 115 | 130 | dB | | |
| Quiescent Current | | I_Q | $Vo = Vs/2$ | | 0.75 | 1.0 | mA | |
| | | | $-40^{\circ}C \leq TA \leq +125^{\circ}C$ | | 0.9 | 1.2 | | |
| Dynamic Characteristics | | | | | | | | |
| Gain Bandwidth Product | | GBP | $Av = +100$ | | 3.3 | MHz | | |
| Slew Rate | | SR | $Av = +1$, $RL = 10k\Omega$ | | 1.4 | V/ μ s | | |
| Overload Recovery Time | | | | | 0.05 | ms | | |
| Noise Characteristics | | | | | | | | |
| Voltage Noise | | e_n P-P | 0.1Hz to 10Hz | | 0.50 | μV_{p-p} | | |
| Voltage Noise Density | | e_n | $f = 1kHz$ | | 22 | nV/\sqrt{Hz} | | |
| Current Noise Density | | i_n | $f = 10Hz$ | | 5 | fA/\sqrt{Hz} | | |

TYPICAL CURVES


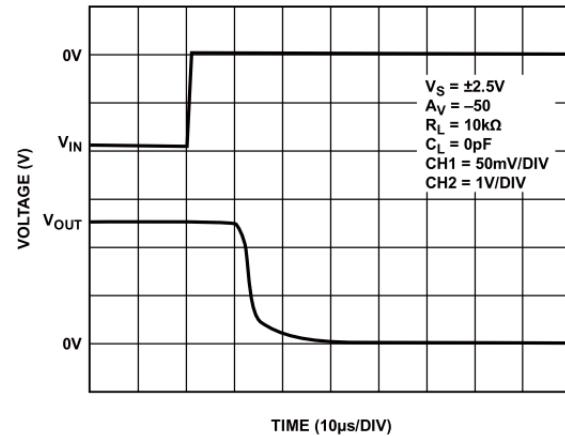
Small Signal Transient Response



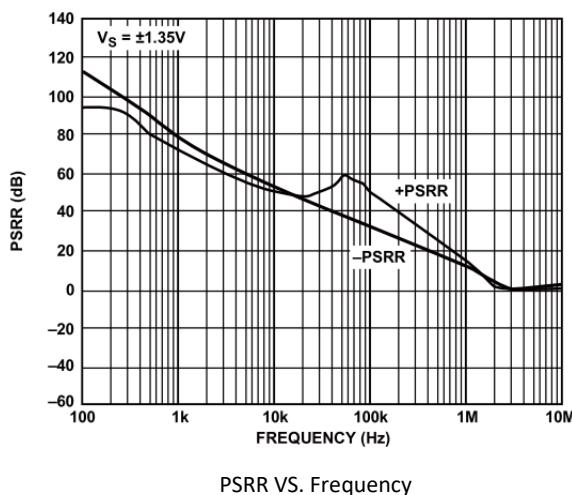
Small Signal Transient Response



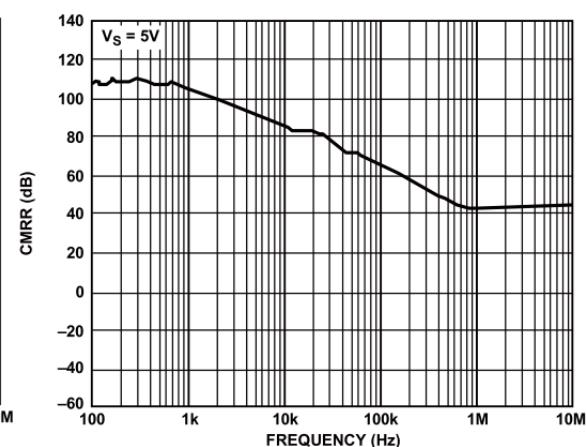
Positive Overvoltage Recovery Time



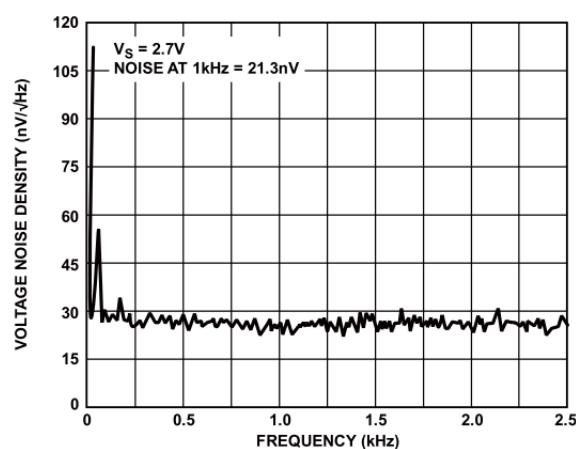
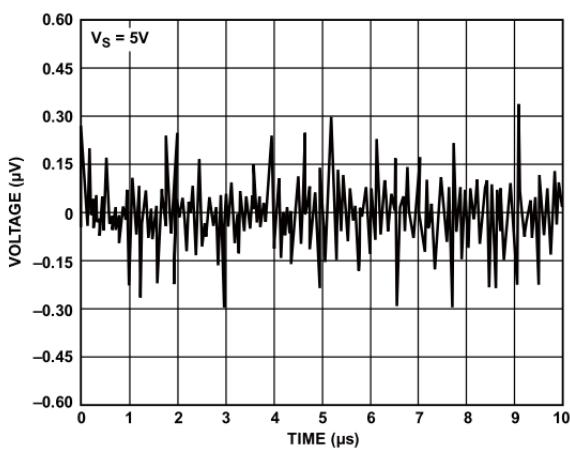
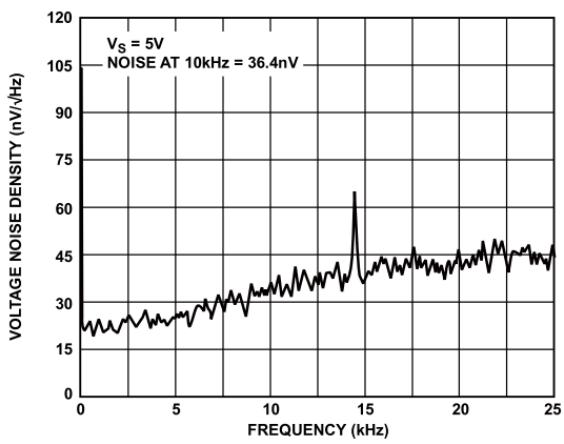
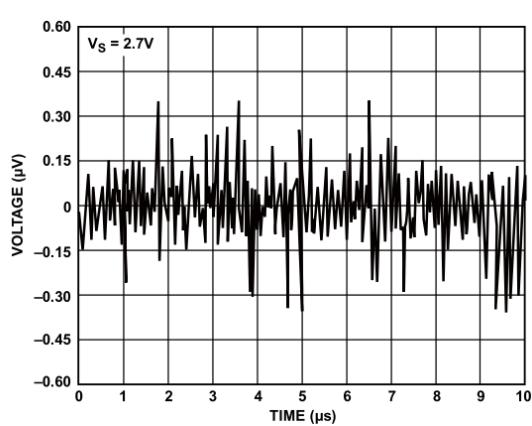
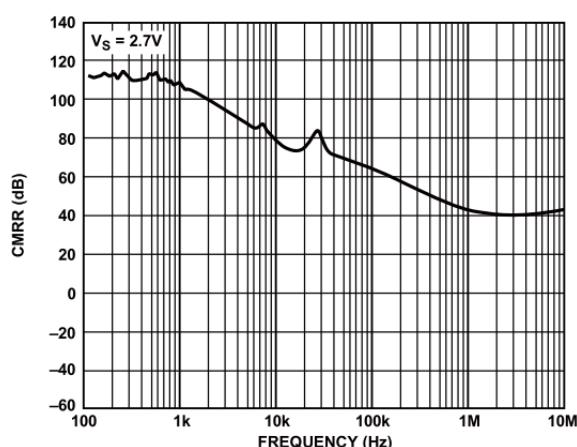
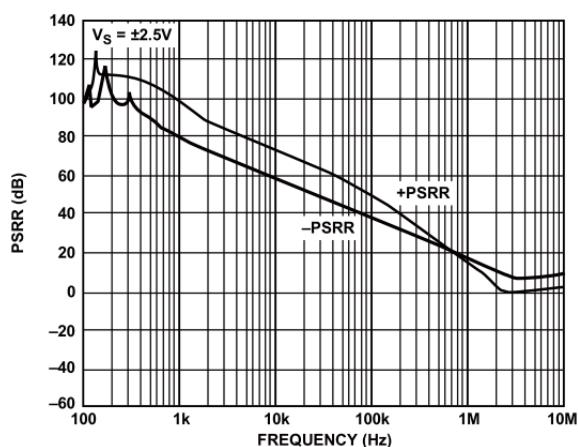
Negative Overvoltage Recovery Time

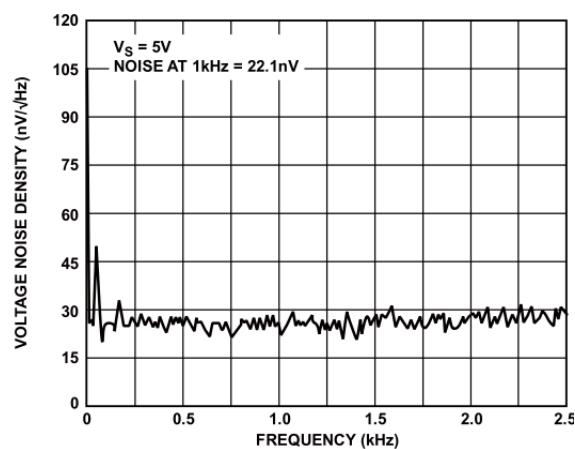


PSRR VS. Frequency

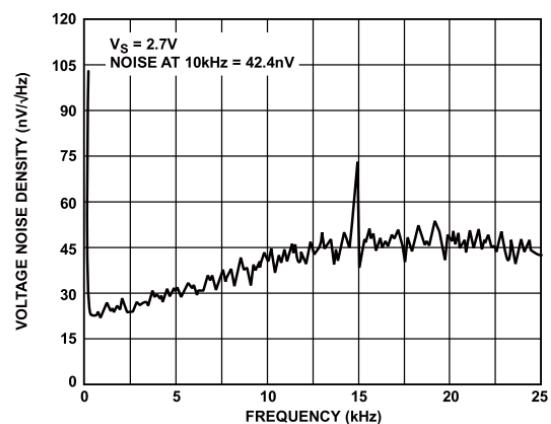


CMRR VS. Frequency





0Hz to 2.5kHz Voltage Noise Density on 5V



0Hz to 25kHz Voltage Noise Density on 2.7V

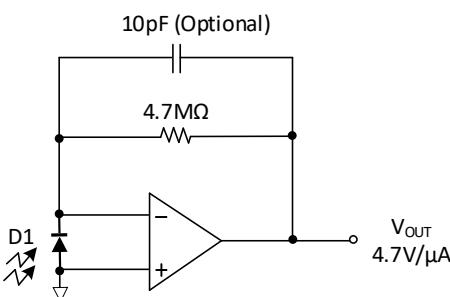
TYPICAL APPLICATIONS

High Source Impedance Application

The CMOS rail-to-rail input structure makes the input current ultra-low, the typical value 0.2 pA. Therefore, the MS860x could be used in some applications ,which have high source impedance or need large resistance around amplifier. For example, the low input bias current operational amplifier is required in photodiode amplifier circuit as shown below, in order to reduce output voltage error. The low input bias current and offset voltage of the MS8601 could reduce the offset error to the minimum. The current through the photodiode is directly proportional to the light power on surface. The 4.7MΩ resistance converts the current to voltage. Thus the MS8601 output increases at 4.7V/μA. The feedback capacitance constrains the circuit bandwidth to reduce excessive noise on high frequency:

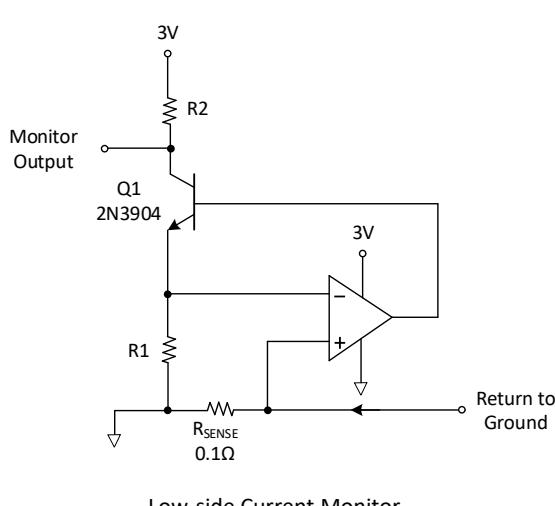
$$BW = \frac{1}{2\pi(4.7M\Omega)C_F}$$

The 10pF feedback capacitance could constrain the bandwidth to about 3.3 kHz.

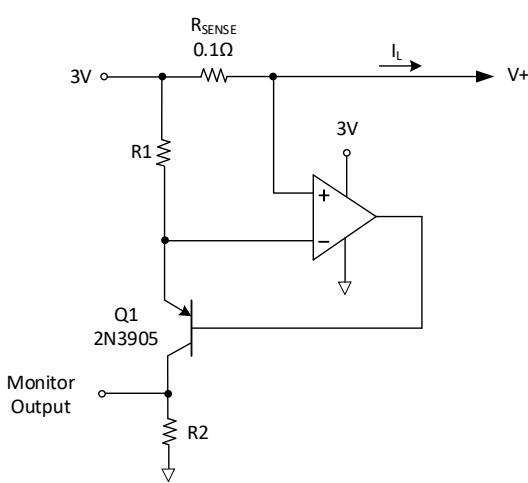


High-side and Low-side Precise Current Monitor

Due to the low input bias current and offset voltage, the MS860x could be applied to precise current monitor. The real rail-to-rail input characteristic allows the MS860x to monitor the current on high-side or low-side. Applying the two amplifiers of the MS8602, easily monitors current supply and return paths whether load or fault, as shown in following two circuits.



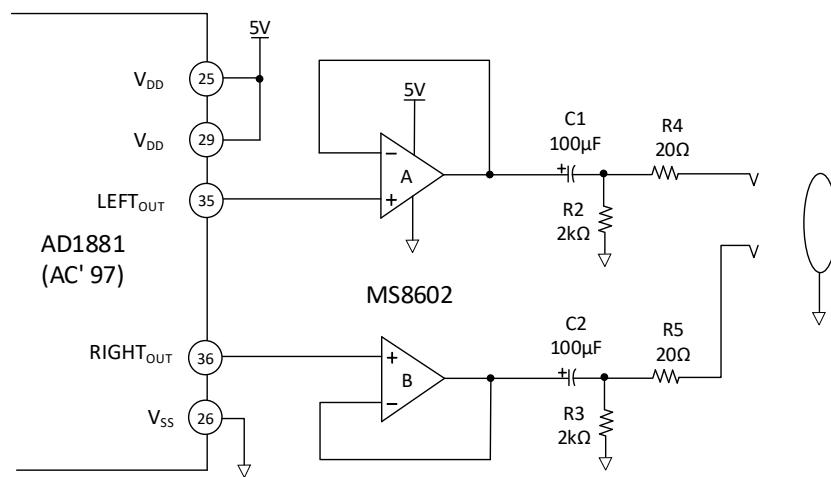
Low-side Current Monitor

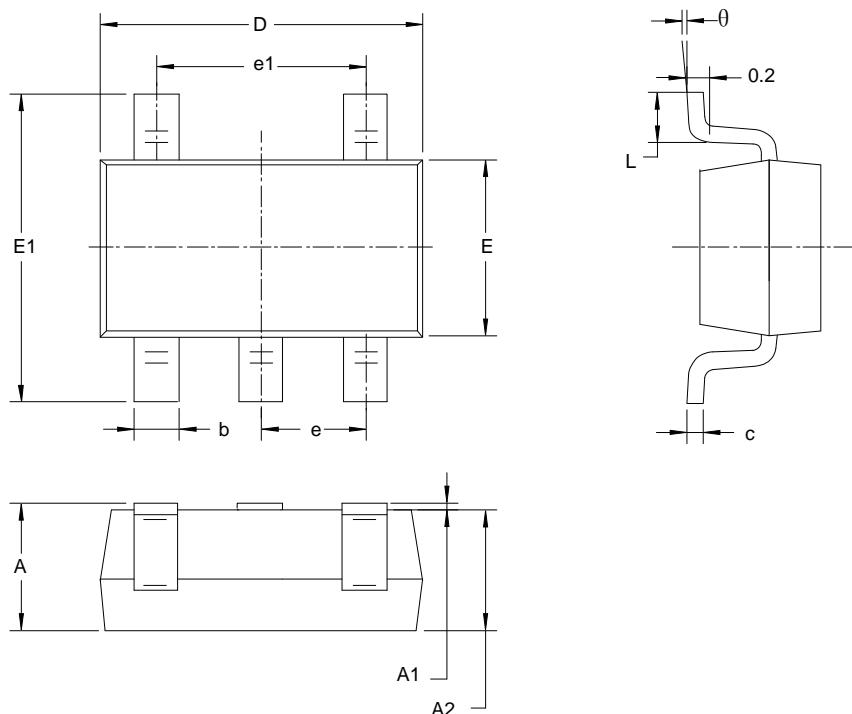


High-side Current Monitor

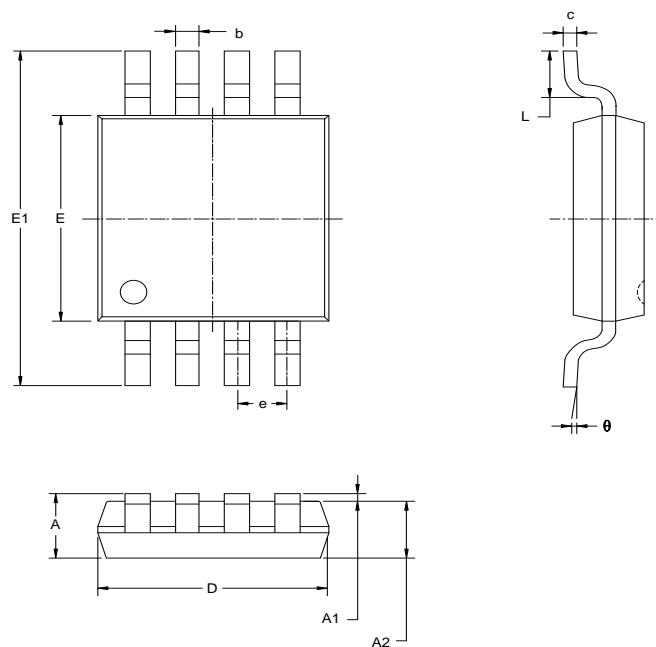
Compatible with PC100 Computer Audio Application

Because of low distortion and rail-to-rail input/output features, the MS860x is the optimal selection for various low cost and single power audio applications, from microphone magnifying to line output buffer. In unit gain, the typical THD+N is 0.004% or -86dB and the load resistance could be up to 600Ω. And it meets the PC100 specification about the requirements for portable and desktop computer audio. The following diagram shows the how to interface with AC'97 codec to drive line output. The MS8602 is used as unit gain buffer of the left and right outputs of the AC'97 code. The 100μF output coupling capacitor is used as blocking capacitor, and 20Ω series resistor protects amplifier from the jack short-circuit influence.

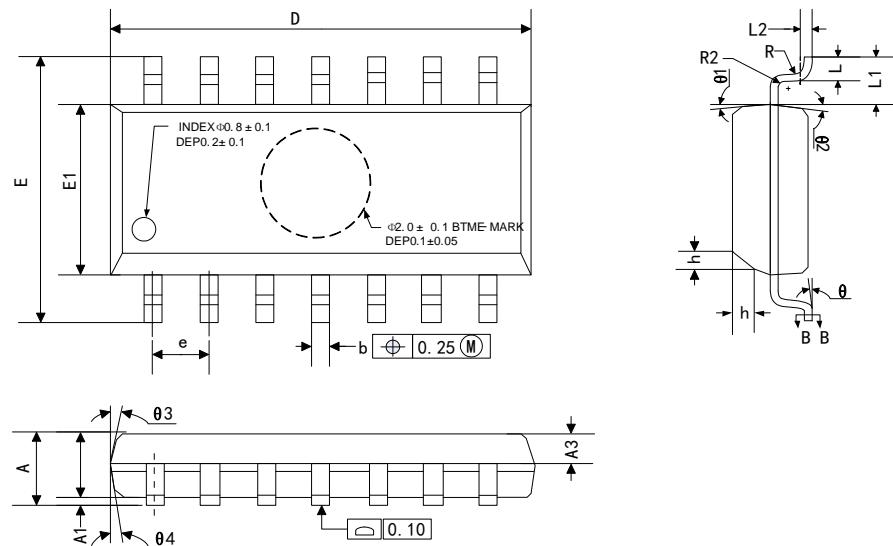


PACKAGE OUTLINE DIMENSIONS
SOT23-5


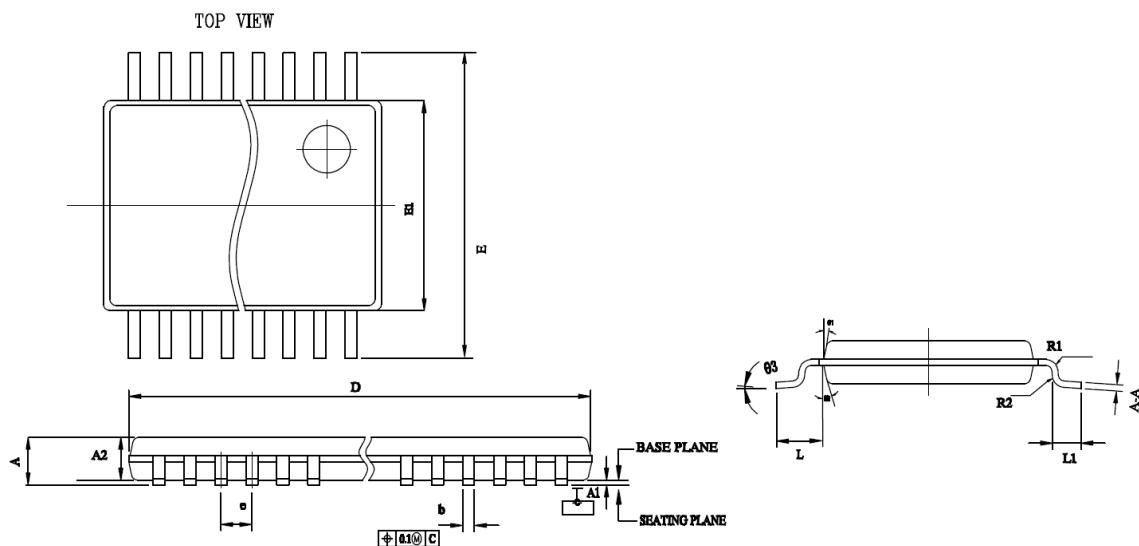
| Symbol | Dimensions in Millimeters | | Dimensions in Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min | Max | Min | Max |
| A | 1.050 | 1.250 | 0.041 | 0.049 |
| A1 | 0.000 | 0.100 | 0.000 | 0.004 |
| A2 | 1.050 | 1.150 | 0.041 | 0.045 |
| b | 0.300 | 0.500 | 0.012 | 0.020 |
| c | 0.100 | 0.200 | 0.004 | 0.008 |
| D | 2.820 | 3.020 | 0.111 | 0.119 |
| E | 1.500 | 1.700 | 0.059 | 0.067 |
| E1 | 2.650 | 2.950 | 0.104 | 0.116 |
| e | 0.950 BSC | | 0.037 BSC | |
| e1 | 1.900 BSC | | 0.075 BSC | |
| L | 0.300 | 0.600 | 0.012 | 0.024 |
| θ | 0° | 8° | 0° | 8° |

MSOP8


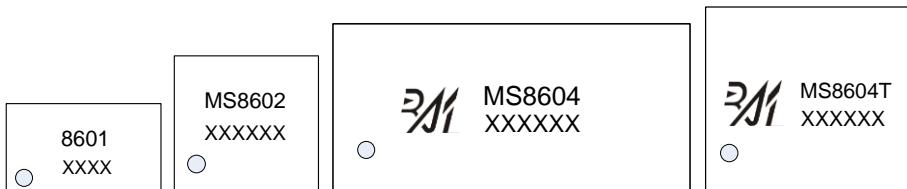
| Symbol | Dimensions in Millimeters | | Dimensions in Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min | Max | Min | Max |
| A | 0.820 | 1.100 | 0.032 | 0.043 |
| A1 | 0.020 | 0.150 | 0.001 | 0.006 |
| A2 | 0.750 | 0.950 | 0.030 | 0.037 |
| b | 0.250 | 0.380 | 0.010 | 0.015 |
| c | 0.090 | 0.230 | 0.004 | 0.009 |
| D | 2.900 | 3.100 | 0.114 | 0.122 |
| E | 2.900 | 3.100 | 0.114 | 0.122 |
| E1 | 4.750 | 5.050 | 0.187 | 0.199 |
| e | 0.650BSC | | 0.026BSC | |
| L | 0.400 | 0.800 | 0.016 | 0.031 |
| θ | 0° | 6° | 0° | 6° |

SOP14


| Symbol | Dimensions in Millimeters | | |
|--------|---------------------------|-----|------|
| | Min | Typ | Max |
| A | 1.35 | | 1.75 |
| A1 | 0.10 | | 0.25 |
| A2 | 1.25 | | 1.65 |
| A3 | 0.55 | | 0.75 |
| D | 8.53 | | 8.73 |
| E | 5.80 | | 6.20 |
| E1 | 3.80 | | 4.00 |
| e | 1.27 BSC | | |
| L | 0.45 | | 0.80 |
| L1 | 1.04 REF | | |
| L2 | 0.25 BSC | | |
| R | 0.07 | | |
| R1 | 0.07 | | |
| h | 0.30 | | 0.50 |
| θ | 0 ° | | 8 ° |
| θ1 | 6 ° | 8° | 10 ° |
| θ2 | 6 ° | 8° | 10 ° |
| θ3 | 5 ° | 7° | 9 ° |
| θ4 | 5 ° | 7° | 9 ° |

TSSOP14


| Symbol | Dimensions in Millimeters | |
|--------|---------------------------|------|
| | Min | Max |
| A | | 1.2 |
| A1 | 0.05 | 0.15 |
| A2 | 0.8 | 1.05 |
| E | 6.25 | 6.55 |
| E1 | 4.3 | 4.5 |
| D | 4.9 | 5.1 |
| L | | 1 |
| L1 | 0.45 | 0.75 |
| e | 0.65 | |
| b | 0.19 | 0.3 |
| R1 | 0.15TYP | |
| R2 | 0.15TYP | |
| A-A | 0.09 | 0.2 |
| θ1 | 12°TYP | |
| θ2 | 12°TYP | |
| θ3 | 0 | 8° |

MARKING and PACKAGING SPECIFICATIONS**1. Marking Drawing Description**

Product Name : 8601, MS8602, MS8604, MS8604T

Product Code : XXXX, XXXXXX

2. Marking Drawing Demand

Laser printing, contents in the middle, font type Arial.

3. Packaging Specifications

| Device | Package | Piece/Reel | Reel/Box | Piece /Box | Box/Carton | Piece/Carton |
|---------|---------|------------|----------|------------|------------|--------------|
| MS8601 | SOT23-5 | 3000 | 10 | 30000 | 4 | 120000 |
| MS8602 | MSOP8 | 3000 | 1 | 3000 | 8 | 24000 |
| MS8604 | SOP14 | 2500 | 1 | 2500 | 8 | 20000 |
| MS8604T | TSSOP14 | 3000 | 1 | 3000 | 8 | 24000 |

STATEMENT

- All Revision Rights of Datasheets Reserved for Ruimeng. Don't release additional notice.
Customer should get latest version information and verify the integrity before placing order.
- When using Ruimeng products to design and produce, purchaser has the responsibility to observe safety standard and adopt corresponding precautions, in order to avoid personal injury and property loss caused by potential failure risk.
- The process of improving product is endless. And our company would sincerely provide more excellent product for customer.



MOS CIRCUIT OPERATION PRECAUTIONS

Static electricity can be generated in many places. The following precautions can be taken to effectively prevent the damage of MOS circuit caused by electrostatic discharge:

1. The operator shall ground through the anti-static wristband.
2. The equipment shell must be grounded.
3. The tools used in the assembly process must be grounded.
4. Must use conductor packaging or anti-static materials packaging or transportation.



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