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Vishay Semiconductors

# High Speed Infrared Emitting Diode, 850 nm, **Surface Emitter Technology**



#### **DESCRIPTION**

As part of the SurfLight™ portfolio, the VSMY2853SL is an infrared, 850 nm, side looking emitting diode based on GaAlAs surface emitter chip technology with extreme high radiant intensities, high optical power and high speed, molded in clear, untinted plastic packages (with lens) for surface mounting (SMD).

#### **APPLICATIONS**

- IrDA compatible data transmission
- Miniature light barrier
- Photointerrupters
- · Optical switch
- Emitter source for proximity sensors
- IR illumination

### **FEATURES**

· Package type: surface mount

· Package form: side view

Peak wavelength: λ<sub>p</sub> = 850 nm

High reliability

High radiant power

· Very high radiant intensity

• Angle of half intensity:  $\varphi = \pm 28^{\circ}$ 

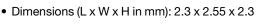
· Suitable for high pulse current operation

 Package matches with detector VEMD2xx3SLX01 and VEMT2xx3SLX01 series

Floor life: 4 weeks, MSL 2a, acc. J-STD-020

· Material categorization: For definitions of compliance please see www.vishav.com/doc?99912







HALOGEN FREE **GREEN** 

COMPLIANT

- IR touch panels
- 3D TV

PRODUCT SUMMARY					
COMPONENT	I <sub>e</sub> (mW/sr)	φ (deg)	$\lambda_{\mathbf{p}}$ (nm)	t <sub>r</sub> (ns)	
VSMY2853SL	35	± 28	850	10	

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSMY2853SL	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	Side view		

· MOQ: minimum order quantity



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V <sub>R</sub>	5	V
Forward current		I <sub>F</sub>	100	mA
Peak forward current	$t_p/T = 0.5, t_p = 100 \ \mu s$	I <sub>FM</sub>	200	mA
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	1	А
Power dissipation		P <sub>V</sub>	190	mW
Junction temperature		T <sub>j</sub>	100	°C
Operating temperature range		T <sub>amb</sub>	- 40 to + 85	°C
Storage temperature range		T <sub>stg</sub>	- 40 to + 100	°C
Soldering temperature	acc. figure 7, J-STD-020	T <sub>sd</sub>	260	°C
Thermal resistance junction/ambient	J-STD-051, soldered on PCB	R <sub>thJA</sub>	250	K/W

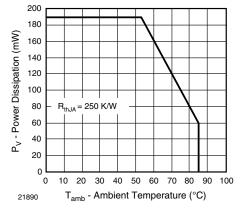


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

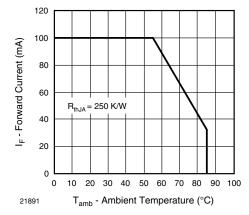


Fig. 2 - Forward Current Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	V <sub>F</sub>		1.65	1.9	V
	I <sub>F</sub> = 1 A, t <sub>p</sub> = 100 μs	V <sub>F</sub>		2.9		V
Town every seafficient of \/	I <sub>F</sub> = 1 mA	TK <sub>VF</sub>		- 1.45		mV/K
Temperature coefficient of V <sub>F</sub>	I <sub>F</sub> = 10 mA	TK <sub>VF</sub>		- 1.3		mV/K
Reverse current		I <sub>R</sub>	not designed for reverse operation		μΑ	
Junction capacitance	$V_R = 0 \text{ V, f} = 1 \text{ MHz, E} = 0 \text{ mW/cm}^2$	СЈ		125		pF
Dedicatists with	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	l <sub>e</sub>	20	35	50	mW/sr
Radiant intensity	I <sub>F</sub> = 1 A, t <sub>p</sub> = 100 μs	I <sub>e</sub>		300		mW/sr
Radiant power	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	фе		55		mW
Temperature coefficient of radiant power	I <sub>F</sub> = 100 mA	TKφ <sub>e</sub>		- 0.35		%/K
Angle of half intensity		φ		± 28		deg
Peak wavelength	I <sub>F</sub> = 100 mA	λρ	840	850	870	nm
Spectral bandwidth	I <sub>F</sub> = 30 mA	Δλ		30		nm
Temperature coefficient of λ <sub>p</sub>	I <sub>F</sub> = 30 mA	TKλ <sub>p</sub>		0.25		nm/K
Rise time	I <sub>F</sub> = 100 mA, 20 % to 80 %	t <sub>r</sub>		10		ns
Fall time	I <sub>F</sub> = 100 mA, 20 % to 80 %	t <sub>f</sub>		10		ns

### **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

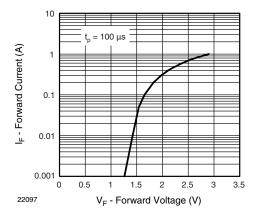


Fig. 3 - Forward Current vs. Forward Voltage

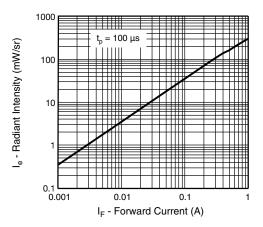


Fig. 4 - Radiant Intensity vs. Forward Current

**SOLDER PROFILE** 

#### max. 260 255 °C 250 -245 °C -240 °C -217 °C Femperature (°C) 200 max. 30 s 150 max. 120 s max. 100 s 100 max. ramp down 6 °C/s 50 max. ramp up 3 °C/s 0 0 100 150 200 250 Time (s)

Fig. 7 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

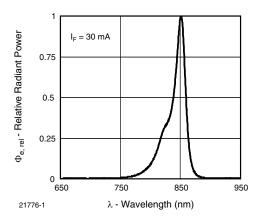


Fig. 5 - Relative Radiant Power vs. Wavelength

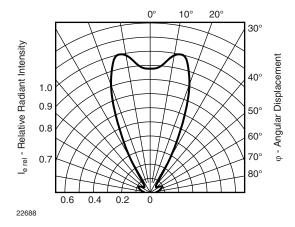


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

#### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

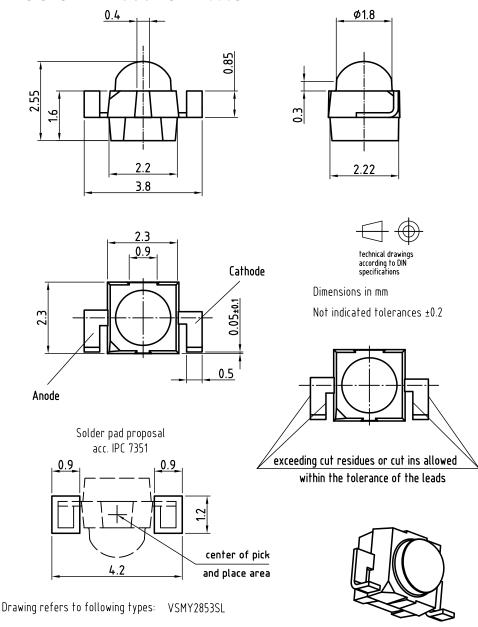
Moisture sensitivity level 2a, acc. to J-STD-020.

#### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40  $^{\circ}$ C (+ 5  $^{\circ}$ C), RH < 5  $^{\circ}$ M.



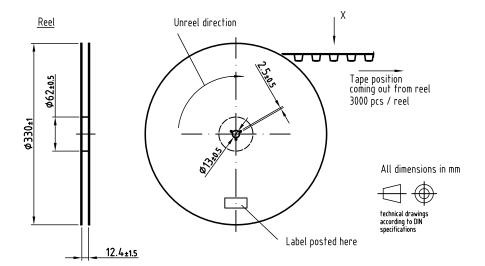
#### PACKAGE DIMENSIONS in millimeters: VSMY2853SL



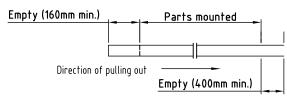
Drawing-No.: 6.544-5410.03-4

Issue: prel. 03.08.12

#### TAPING AND REEL DIMENSIONS in millimeters: VSMY2853SL

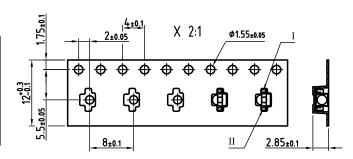


#### Leader and trailer tape:



erminal	. position in tape	

Device	Lead I	Lead II	
VSMB2943SLX01			
VSMF2893SLX01			
VSMB2948SL	Cathode	Anode	
VEMD2023SLX01			
VEMD2523SLX01			
VEMT2023SLX01	C.11	F=:11	
VEMT2523SLX01	Collector	Emitter	
VSMY2853SL	Anode	Cathode	



Drawing refers to following types: see table Reel dimensions and tape

Drawing-No.: 9.800-5123.01-4 Issue: prel; 01.02.13



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