Unit: mm

1.6MAX

 0.4 ± 0.05

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L^2 - π -MOSV)

2SK2615

DC-DC Converter, Relay Drive and Motor Drive Applications

• Low drain-source ON resistance : RDS (ON) = 0.23Ω (typ.)

• High forward transfer admittance $|Y_{fs}| = 2.0 \text{ S (typ.)}$

• Low leakage current : $I_{DSS} = 100 \,\mu\text{A} \,(\text{max}) \,(\text{V}_{DS} = 60 \,\text{V})$

• Enhancement mode : $V_{th} = 0.8 \text{ to } 2.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	etics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	60	(\vee)
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	60	V
Gate-source voltage		V_{GSS}	±20	\
Drain current	DC (Note 1)	ID	2	A
	Pulse (Note 1)	I_{DP}	6	A
Drain power dissipation	1	PD	0.5	W
Drain power dissipation (Note 2)		PD	1.5	/ (w
Channel temperature		T _{ch}	150	°C
Storage temperature ra	inge	T _{stg}	-55 to 150	°C

···	4.2M,
0.45 - 0.08	0.8MIN.
+0.08 0.4-0.05	+ 0.08 0.4 - 0.05
1.5 ± 0.1	1.5±0.1
1. GATE 2. DRAIN (HEAT SINK 3. SOURCE	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
JEDEC	_
JEITA	_
TOSHIBA	2-5K1B

4.6MAX

1.7MAX.

Weight: 0.05 g (typ.)

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Mounted on a ceramic substrate (25.4 mm × 25.4 mm × 0.8 mm)

Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

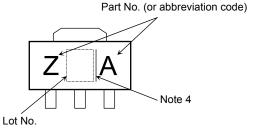
Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	Rth (ch-a)	250	°C/W

This transistor is an electrostatic-sensitive device.

Please handle with caution.

Marking



Note 4: A line to the right of a Lot No. identifies the indication of product Labels.

Without a line: [[Pb]]/INCLUDES > MCV

With a line: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

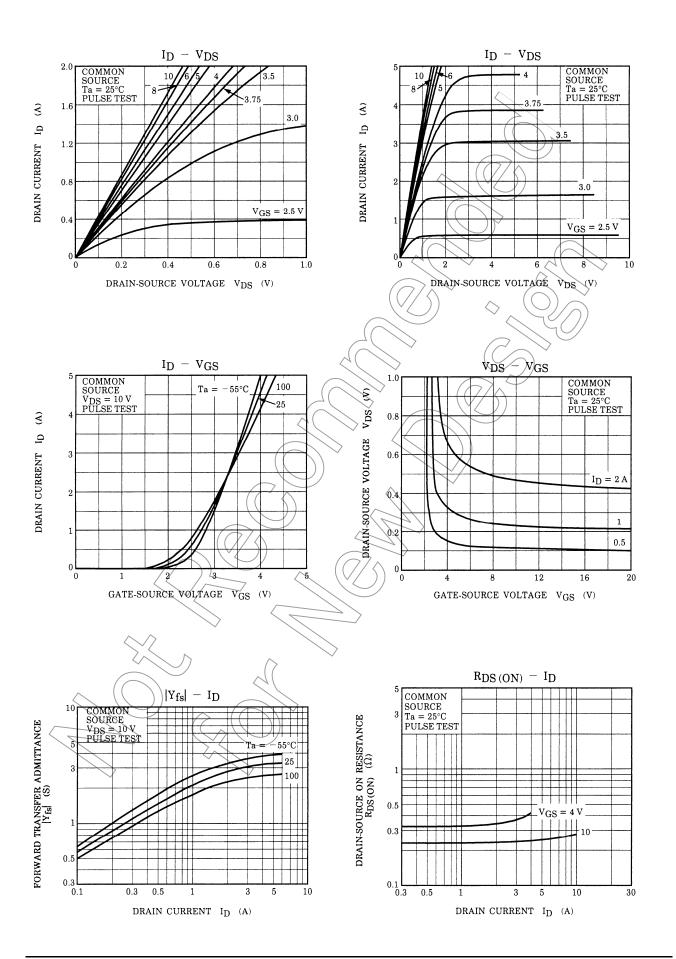
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Electrical Characteristics (Ta = 25°C)

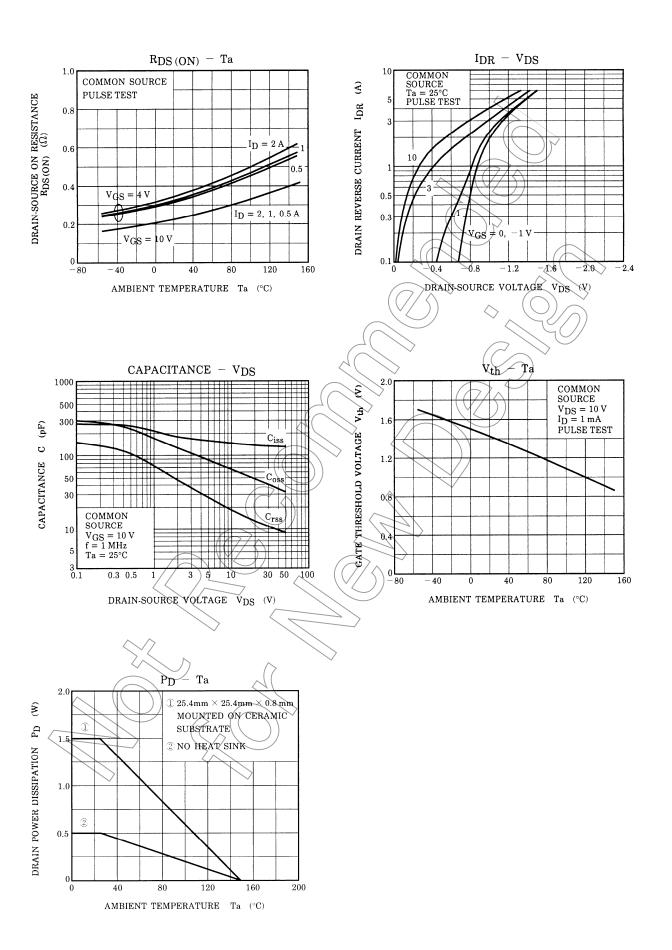
Charae	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source bi	reakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	60	_	_	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	_	2.0	V
Drain-source O	N registance	D=	VGS = 4 V, ID = 1 A	(F	0.33	0.44	Ω
Dialii-souice O	in resistance	R _{DS} (ON)	VGS = 10 V, ID = 1 A	\nearrow	0.23	0.30	12
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 1 A	<u>)</u> ,	2.0	_	S
Input capacitano	ce	C _{iss}		_	150	_	
Reverse transfe	r capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	25	_	pF
Output capacita	nce	Coss		_	70	_	
	Rise time	t _r	V _{GS} ov I I V _{out}	- (25	<u> </u>	
Conitability at times	Turn-on time	t _{on}	OV RL		30) –	
Switching time	Fall time	t _f		7	50	_	ns
	Turn-off time	t _{off}	$V_{DD} = 30V$ Duty $\leq 1\%$, $t_{W} = 10 \mu s$) -	150	_	
Total gate charg		Qg		_	6.0	_	
Gate-source ch	arge	Q _{gs}	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, V_{D} = 2 \text{ A}$	_	4.6	_	nC
Gate-drain ("mi	ller") Charge	Q _{gd}		_	1.4	_	

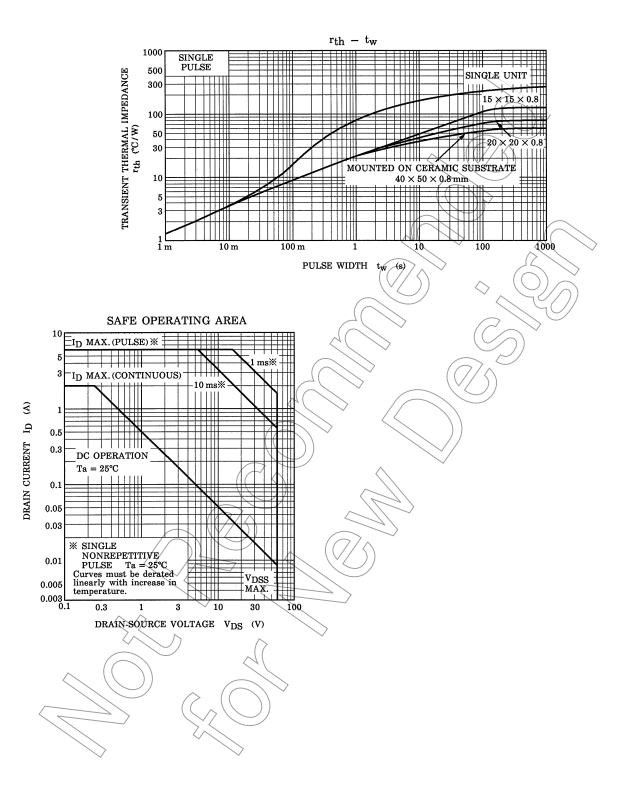
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	1DR	_	ı	_	2	Α
Pulse drain reverse current (Note 1)	\ I _{DRP}	_	l	1	6	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 2 A, V _{GS} = 0 V		_	-1.5	٧
Reverse recovery time		I _{DR} = 2 A, V _{GS} = 0 V	ı	100	1	ns
Reverse recovery charge	Q _{rr}	dl _{DR} / dt = 50 Å / μs		40	_	nC



3 2009-09-29





5 2009-09-29

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