

HD74HC09

Quad. 2-input AND Gates (with open drain outputs)

REJ03D0537-0200 (Previous ADE-205-409) Rev.2.00 Oct 06, 2005

Features

High Speed Operation: t_{pd} = 8 ns typ (C_L = 50 pF)
 High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2$ to 6 V

• Low Input Current: 1 µA max

• Low Quiescent Supply Current: I_{CC} (static) = 1 μA max (Ta = 25°C)

• Ordering Information

Part Name	Package Lyne		Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC09P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74HC09FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74HC09RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)
HD74HC09TELL	TSSOP-14 pin	PTSP0014JA-B (TTP-14DV)	Т	ELL (2,000 pcs/reel)

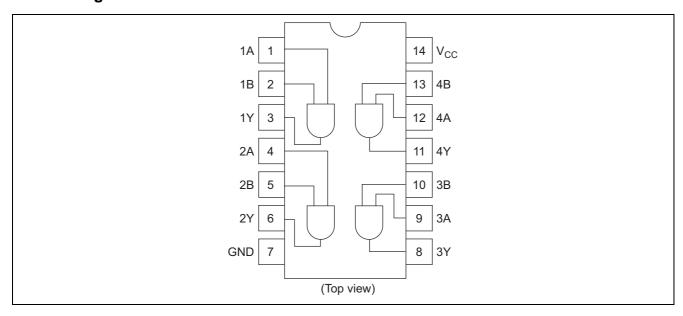
Note: Please consult the sales office for the above package availability.

Function Table

li li	Output	
Α	В	Y
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

H: High levelL: Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
Input / Output voltage	Vin, Vout	-0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	I ₀	±25	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±50	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	2 to 6	V	
Input / Output voltage	V _{IN} , V _{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
	t _r , t _f	0 to 1000		V _{CC} = 2.0 V
Input rise / fall time*1		0 to 500	ns	$V_{CC} = 4.5 \text{ V}$
		0 to 400		$V_{CC} = 6.0 \text{ V}$

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

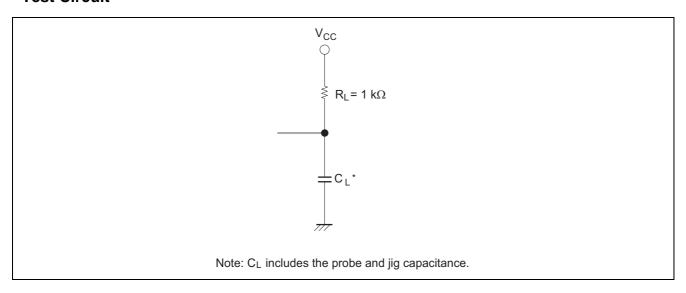
Electrical Characteristics

			Т	a = 25°	С	Ta = -40 to+85°C				
Item	Symbol	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions	
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_			
		6.0	4.2	_	_	4.2	_			
	V _{IL}	2.0		_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35			
		6.0	_	_	1.8	_	1.8			
Output voltage	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	Vin = V_{IH} or V_{IL} I_{OL} = 20 μ A	
		4.5	_	0.0	0.1	_	0.1			
		6.0	_	0.0	0.1	_	0.1			
		4.5	_	_	0.26	_	0.33		$I_{OL} = 4 \text{ mA}$	
		6.0	_	_	0.26	_	0.33		$I_{OL} = 5.2 \text{ mA}$	
Off-state output	lo(off)	6.0	_	_	±0.5	_	±5.0	μΑ	$Vin = V_{IH} \text{ or } V_{IL},$	
current									Vout = V_{CC} or GND	
Input current	lin	6.0	_	_	±0.1		±1.0	μΑ	$\begin{aligned} & \text{Vin} = \text{V}_{\text{CC}} \text{ or GND} \\ & \text{Vin} = \text{V}_{\text{CC}} \text{ or GND, lout} = 0 \ \mu\text{A} \end{aligned}$	
Quiescent supply current	Icc	6.0		_	1.0	_	10	μА		

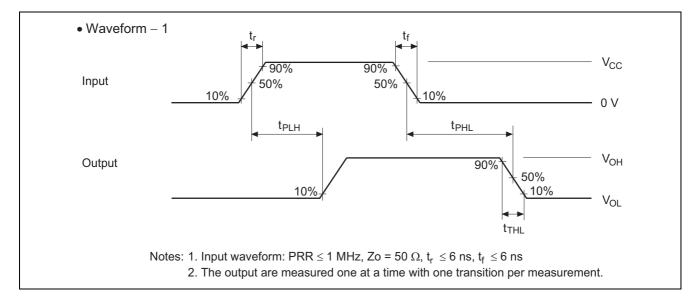
Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

			Т	a = 25°	С	Ta = -40	to +85°C		
Item	Symbol	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	_	90	_	115	ns	
time		4.5	_	10	18	_	23		
		6.0	_	_	15	_	20		
	t _{PHL}	2.0	_	_	90	_	112	ns	
		4.5		6	18	_	22		
		6.0	_	_	15	_	18		
Output fall time	t _{THL}	2.0	_	_	75	_	95	ns	
		4.5	_	5	15	_	19		
		6.0	_	_	13	_	16		
Input capacitance	Cin	_	_	5	10	_	10	pF	

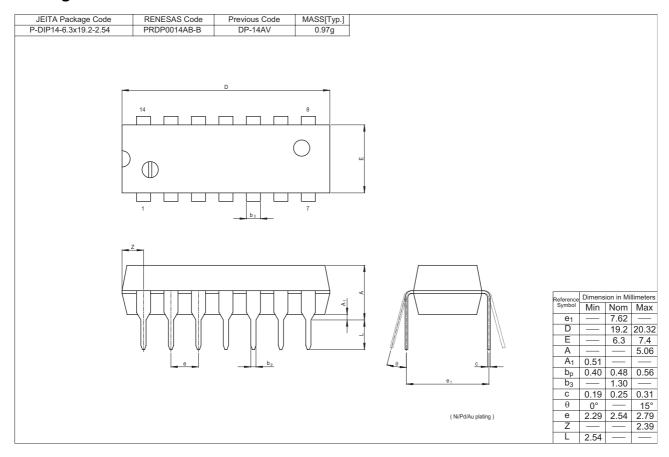
Test Circuit

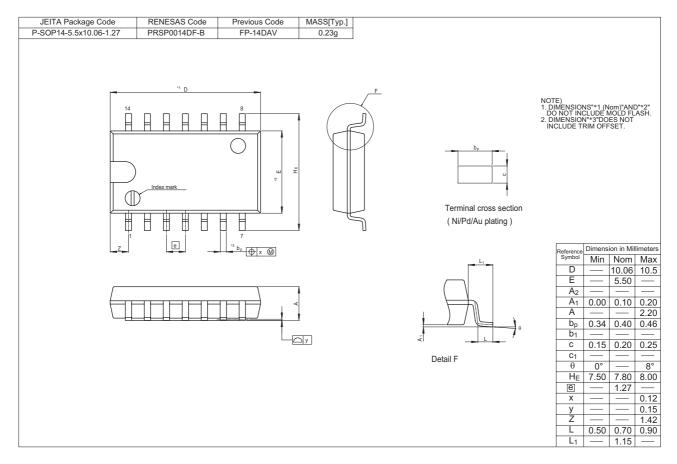


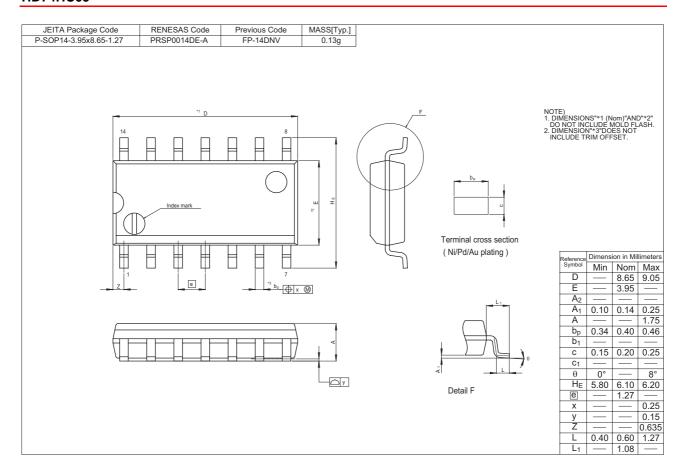
Waveforms

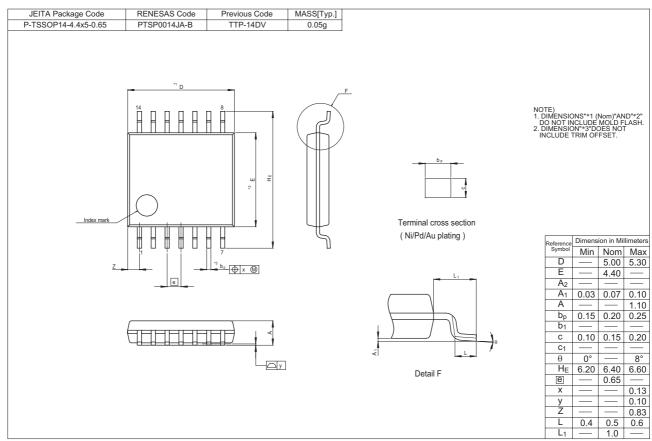


Package Dimensions









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