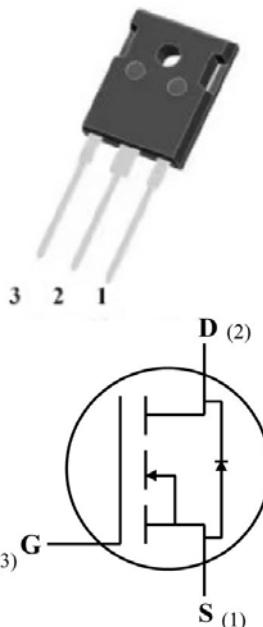


## Silicon Carbide Power MOSFET (N-Channel Enhancement)

$V_{DS}$	1200V
$I_D(25^\circ C)$	38A
$R_{DS(on)}$	80mΩ



### Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free, RoHS compliant

### Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

### Mechanical Data

- **Package:** TO-247AB
- **Terminals:** Tin plated leads
- **Polarity:** As marked

### ■Maximum Ratings ( $T_c=25^\circ C$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code				D212080NCTG1	
Drain source voltage @ $T_j=25^\circ C$	$V_{DS,max}$	V	1200	$V_{GS}=0 V, I_D=100\mu A$	
Gate source voltage @ $T_j=25^\circ C$	$V_{GS,max}$	V	-8/+22	Absolute maximum values	Note1
Gate source voltage @ $T_j=25^\circ C$	$V_{GS,op}$	V	-4/+18	Recommended operational values	Note2
Continuous drain current @ $T_c=25^\circ C$	$I_D$	A	38	$V_{GS}=18V, T_c=25^\circ C$	Fig.18
Continuous drain current @ $T_c=100^\circ C$			28	$V_{GS}=18V, T_c=100^\circ C$	
Pulsed drain current	$I_{D,pulsed}$	A	80	Pulse width $t_p$ limited by $T_{j,max}$	Fig.23
Power Dissipation	$P_{TOT}$	W	220	$T_c=25^\circ C, T_j = 175^\circ C$	Fig.17
Power Dissipation			94	$T_c=110^\circ C, T_j = 175^\circ C$	
Operating junction and Storage temperature range	$T_j, T_{stg}$	°C	-55 to +175		
Soldering temperature	$T_L$	°C	260	1.6mm (0.063") from case for 10s	
Mounting torque	$T_M$	Nm	0.6	M3 screw Maximum of mounting process: 3	



## ■ Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	V <sub>GS(th)</sub>	V	2.3	2.9	3.6	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =5mA	Fig.4, 11
				2.2		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =5mA, Tj=175°C	
Drain source breakdown voltage	V <sub>(BR)DSS</sub>	V	1200			V <sub>GS</sub> =0, I <sub>D</sub> =100uA	
Zero gate voltage drain current	I <sub>DSS</sub>	uA		1	10	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V	Fig.16
Gate source leakage current	I <sub>GSS</sub>	nA			100	V <sub>GS</sub> =18V, V <sub>DS</sub> =0V	
Current drain source on-state resistance	R <sub>DS ON</sub>	mΩ		77	85	V <sub>GS</sub> =18V, I <sub>D</sub> =20A	Fig.5, 6, 7
				122		V <sub>GS</sub> =18V, I <sub>D</sub> =20A, Tj=175°C	
Internal gate resistance	R <sub>g</sub>	Ω		1.5		f=1MHz	
Diode forward voltage	V <sub>SD</sub>	V		3.9		V <sub>GS</sub> =-4V, I <sub>SD</sub> =10A	Fig.8
				3.2		V <sub>GS</sub> =0V, I <sub>SD</sub> =10A Tj=175°C	Fig.9
Transconductance	g <sub>f</sub>	S		10		V <sub>DS</sub> =16V, I <sub>D</sub> =20A	Fig.4
				9.2		V <sub>DS</sub> =16V, I <sub>D</sub> =20A, Tj=175°C	

## ■ Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	C <sub>iss</sub>	pF		890		V <sub>DS</sub> =1000V, V <sub>GS</sub> =0V, Tj=25°C, f=1MHz, V <sub>AC</sub> = 25mV	Fig.13, 14
Output capacitance	C <sub>oss</sub>			58			
Reverse capacitance	C <sub>rss</sub>			4			
C <sub>oss</sub> stored energy	E <sub>oss</sub>	uJ		34			Fig.15
Gate source charge	Q <sub>gs</sub>	nC		12		V <sub>DS</sub> =800V, V <sub>GS</sub> =-4/18V, I <sub>D</sub> =20A	Fig.12
Gate drain charge	Q <sub>gd</sub>			11			
Gate charge	Q <sub>g</sub>			41			

## ■ Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on switching energy	E <sub>on</sub>	uJ		377		V <sub>DD</sub> =800V, V <sub>GS</sub> =-4/+15V, I <sub>D</sub> =20A, R <sub>g</sub> =0Ω, L=16.7uH	Fig.21, 22
Turn off switching energy	E <sub>off</sub>			14			
Turn on delay time	t <sub>d(on)</sub>	ns		21			
Rise time	t <sub>r</sub>			17			
Turn off delay time	t <sub>d(off)</sub>	ns		14		V <sub>DD</sub> =800V, V <sub>GS</sub> =-4/+15V, I <sub>D</sub> =20A, R <sub>g</sub> =0Ω, L=16.7uH	Fig.21, 22
Fall time	t <sub>f</sub>			8			

**■Body diode characteristics (Tc=25°C unless otherwise specified )**

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	$V_{SD}$	V		3.9		$V_{GS}=-4V, I_{SD}=10A$	Fig.8
				3.2		$V_{GS}=0V, I_{SD}=10A, T_j=175^{\circ}C$	Fig.9
Continuous diode forward current	$I_S$	A		38		$T_c=25^{\circ}C$	Note1
Reverse recovery time	trr	nS		28.24		$V_R=800V, V_{GS}=-4V, I_D=20A, \frac{dI}{dt}=2095A/\mu s$	
Reverse recovery charge	Qrr	nC		190			
Peak reverse recovery current	Irrm	A		30.08			

Note 1: When using SiC Body Diode the maximum recommended  $V_{GS} = -4V / +20V$

Note 2: MOSFET can also safely operate at 0/18 V

**■Thermal Characteristics (Ta=25°C Unless otherwise specified)**

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	$R_{\theta J-C}$	°C /W	0.68

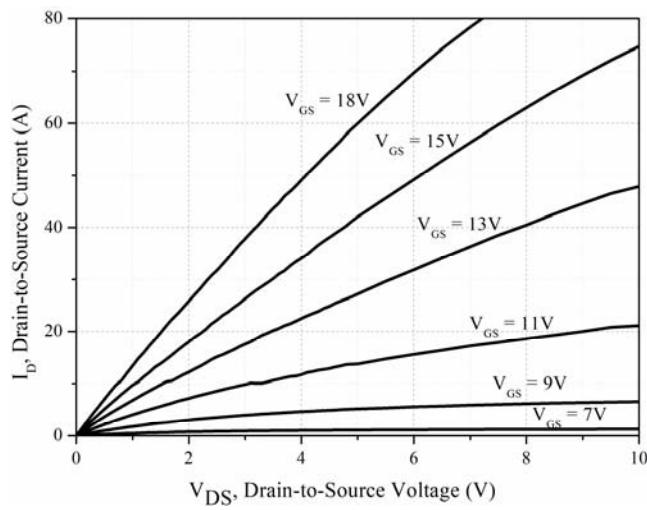
**■Typical Characteristics**


Figure 1. Output Characteristics  $T_j = -40^{\circ}C$

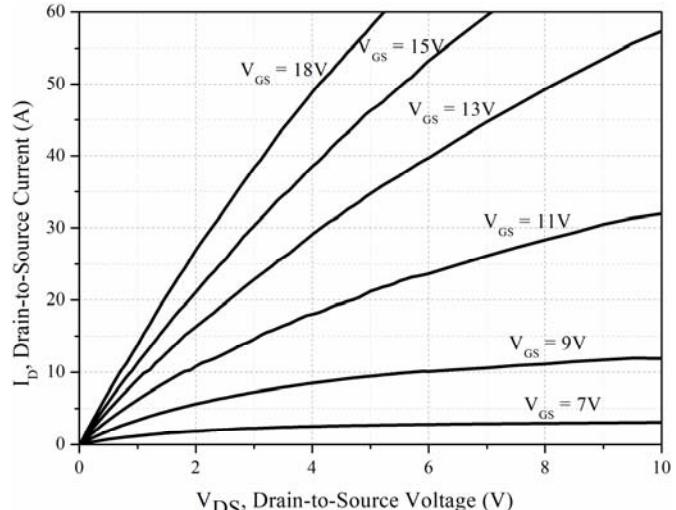


Figure2. Output Characteristics  $T_j = 25^{\circ}C$

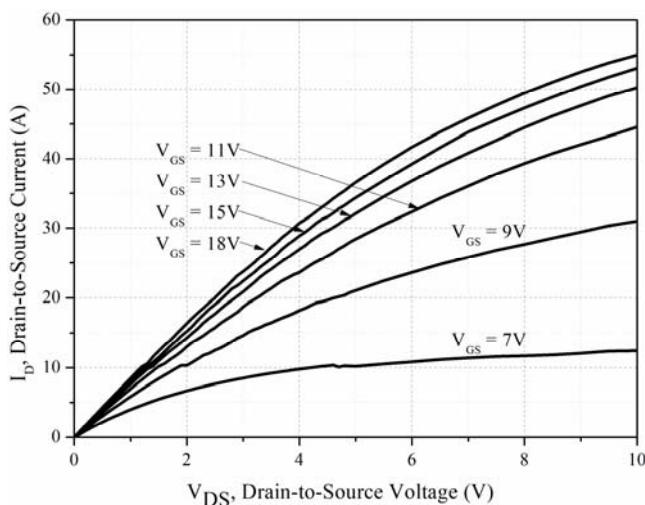


Figure 3. Output Characteristics  $T_j = 175^\circ\text{C}$

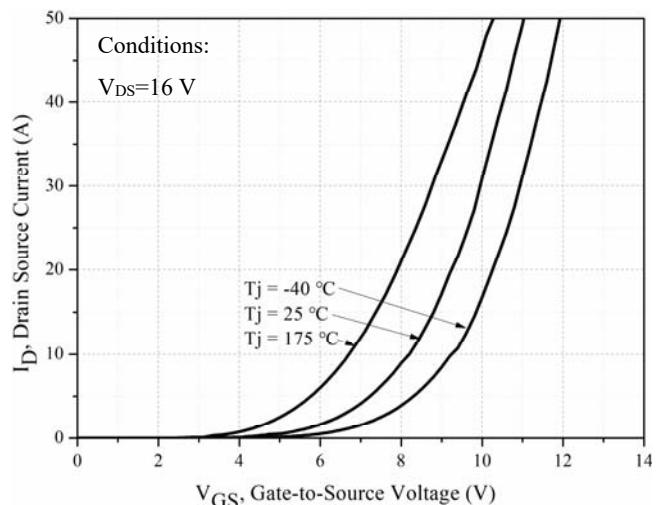


Figure 4. Transfer Characteristics for various junction temperature

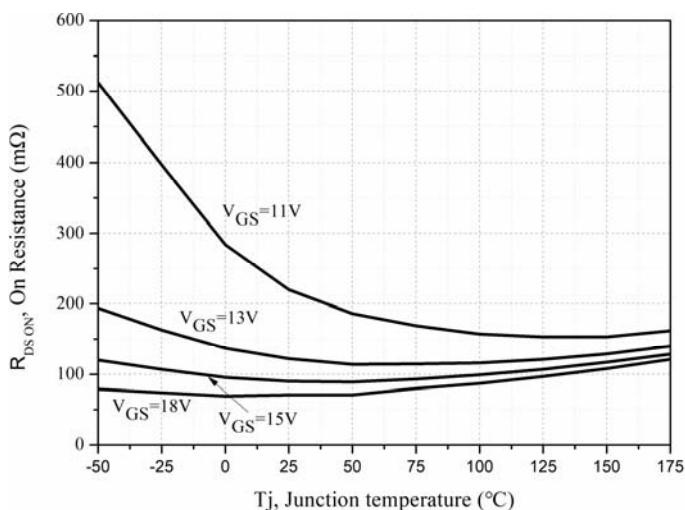


Figure 5. On-resistance vs. temperature for various gate voltage

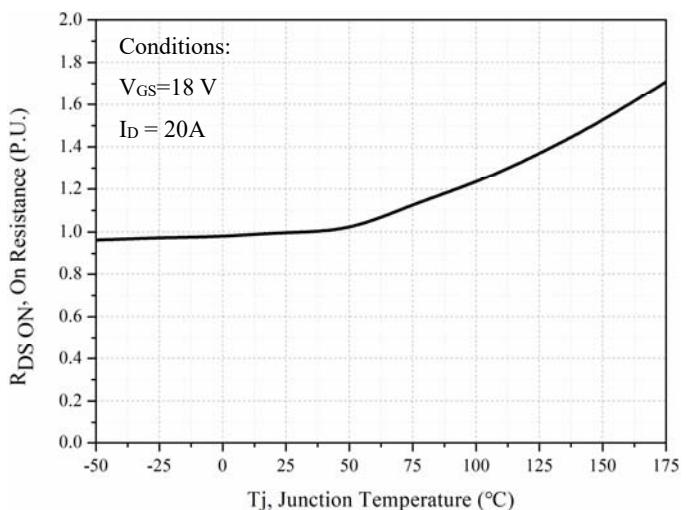


Figure 6. Normalized on-resistance vs. temperature

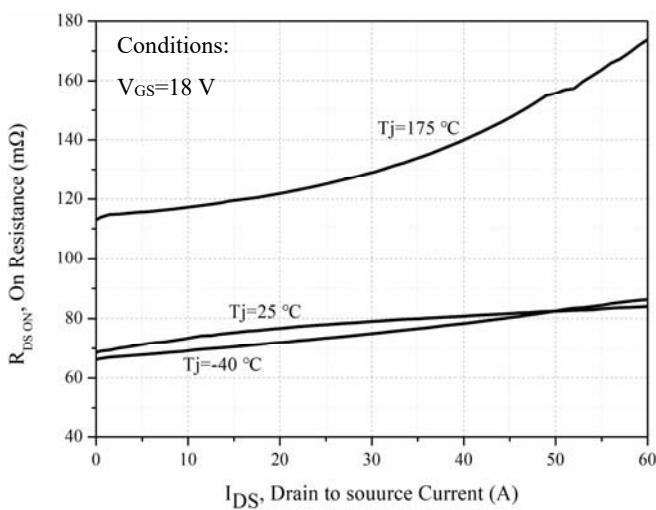


Figure 7. On-resistance vs. drain current

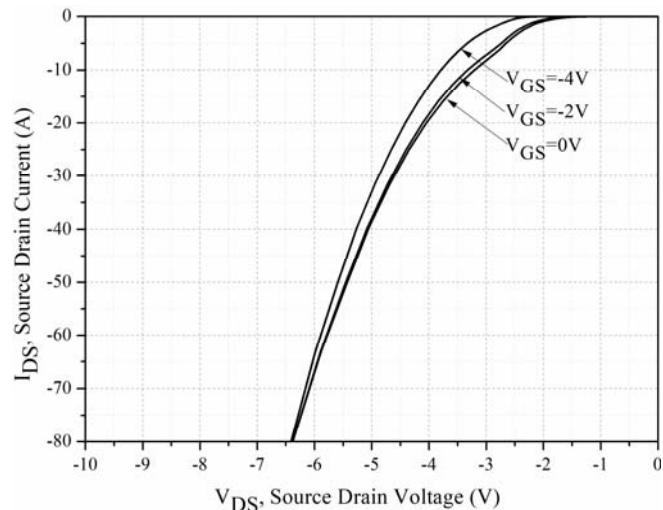


Figure 8. Body diode characteristic at  $T_j = 25^\circ\text{C}$

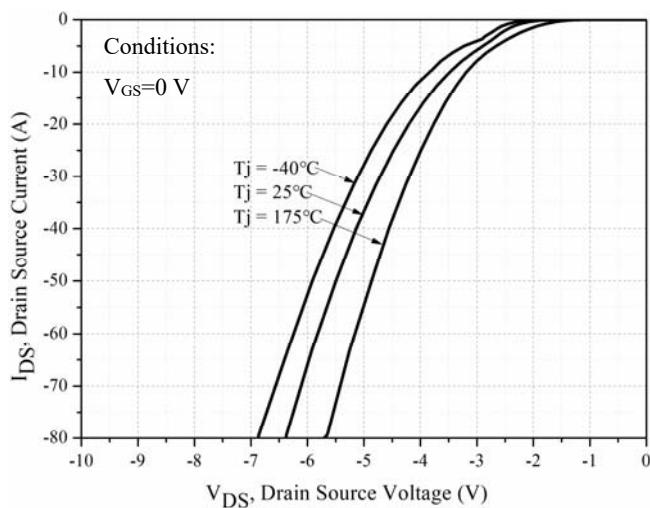


Figure 9. Body diode characteristic

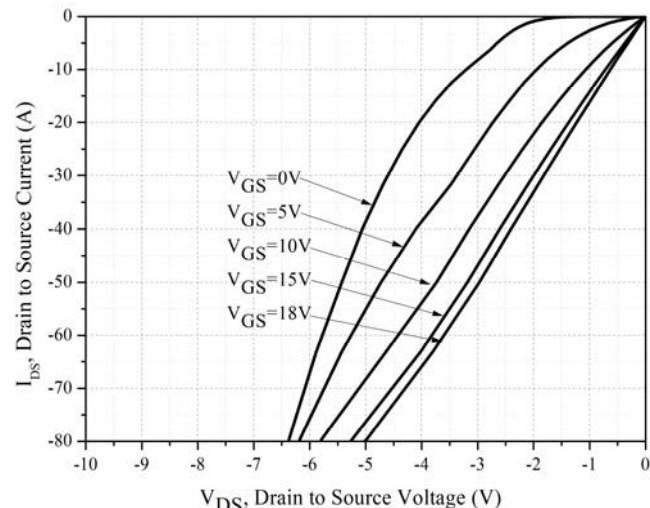
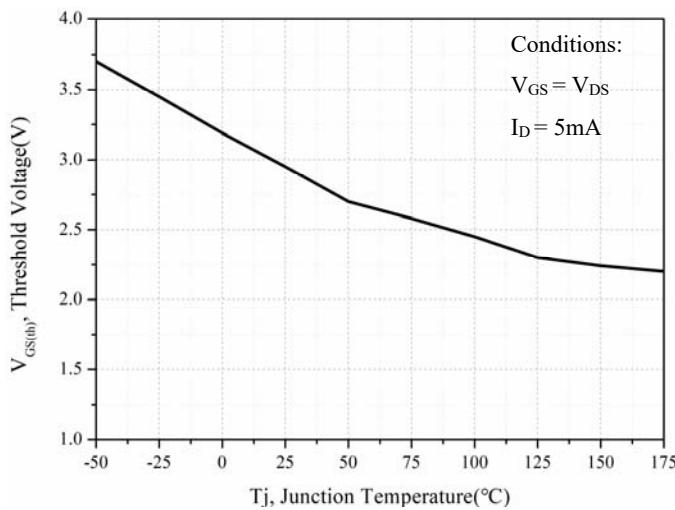

 Figure 10. 3<sup>rd</sup> quadrant characteristic at  $T_j = 25^\circ\text{C}$ 


Figure 11. Threshold voltage vs. temperature

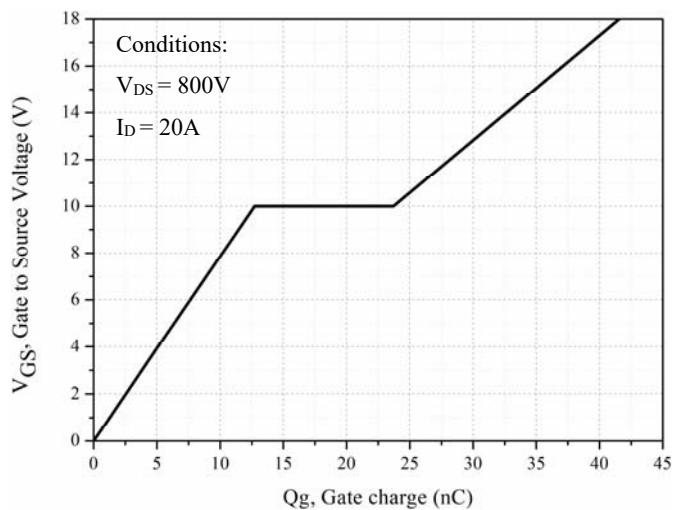


Figure 12. Gate charge characteristic

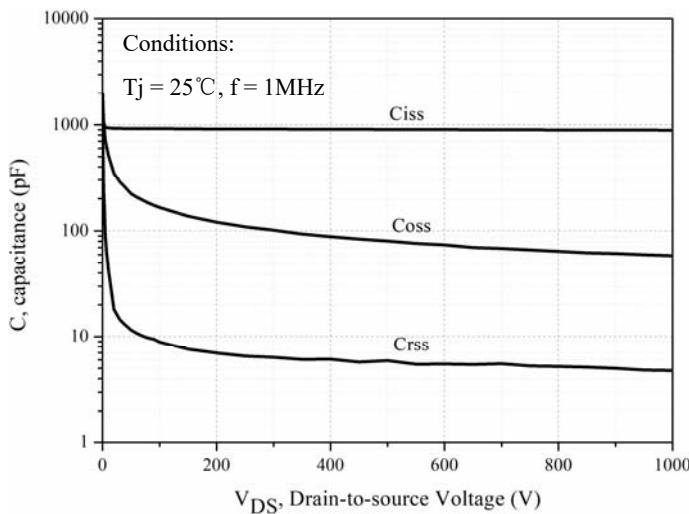


Figure 13. Capacitances vs. drain source voltage (0-1000V)

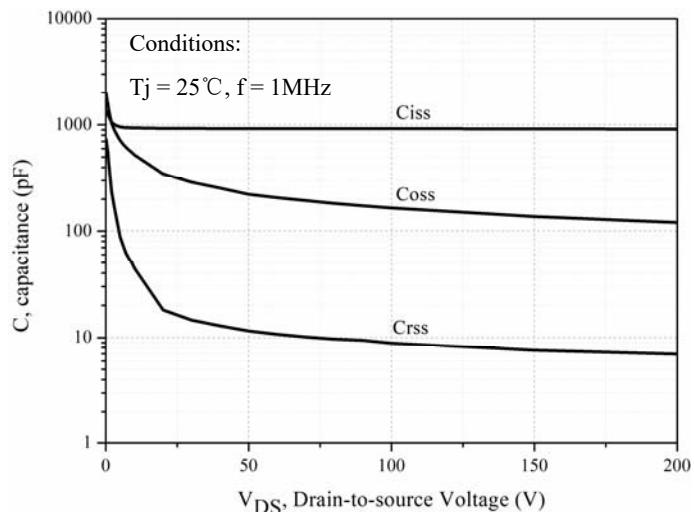
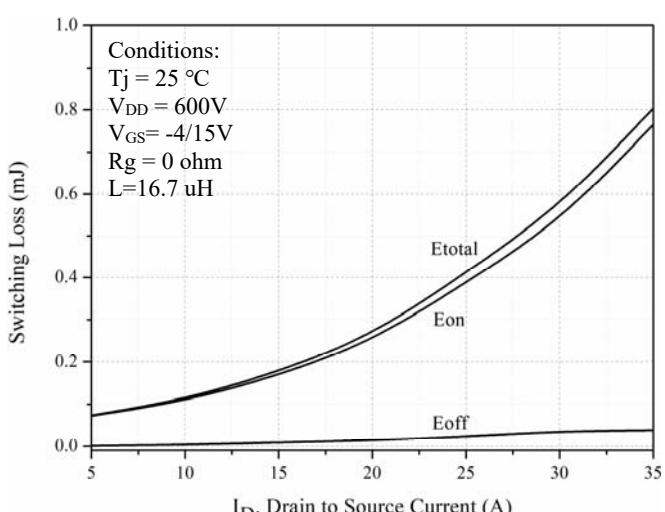
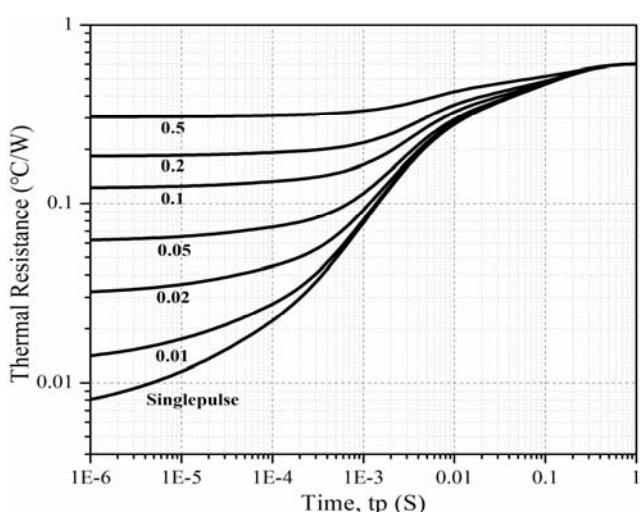
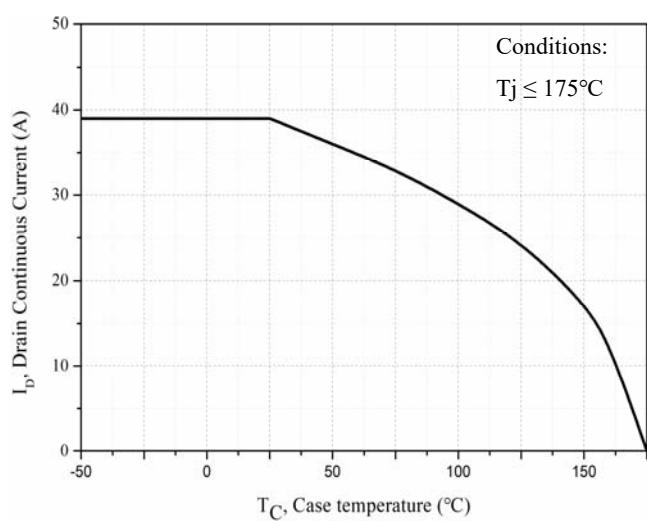
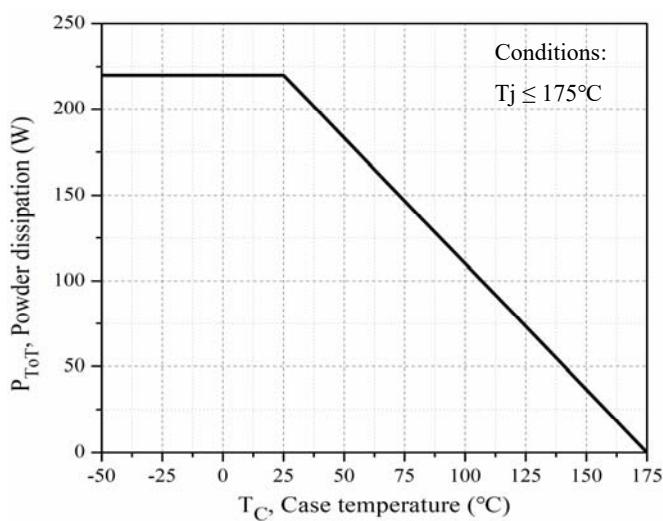
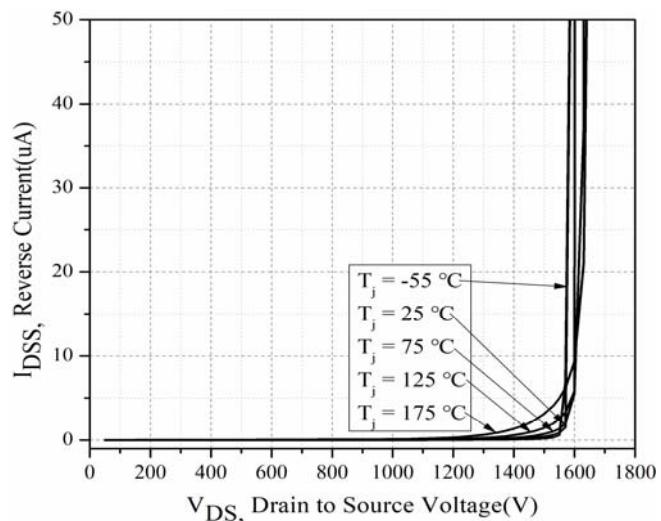
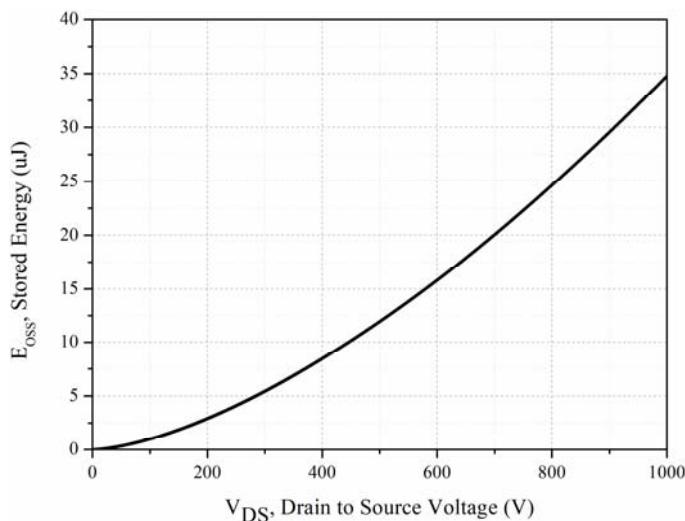


Figure 14. Capacitances vs. drain source voltage (0-200V)



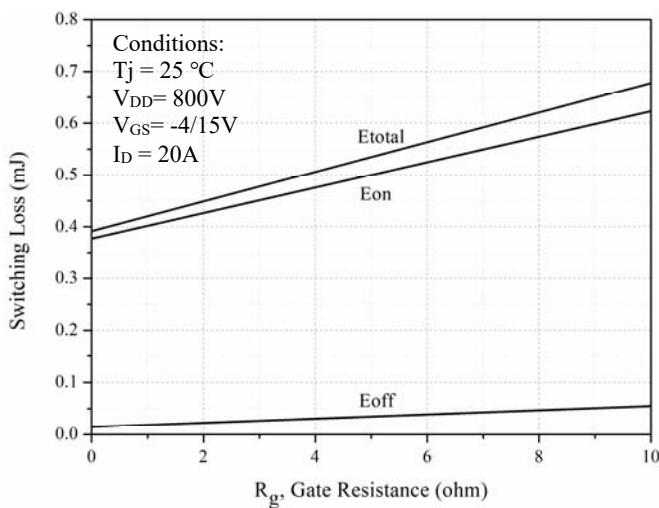


Figure 21. Clamped inductive switching energy vs.  $R_g$

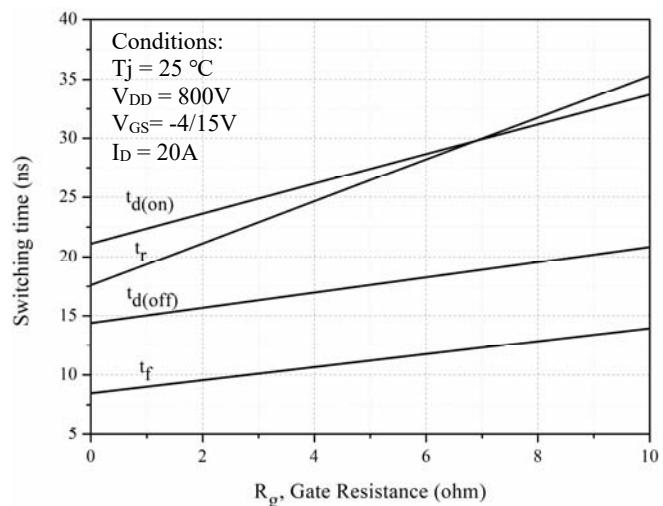


Figure 22. Switching times vs.  $R_g$

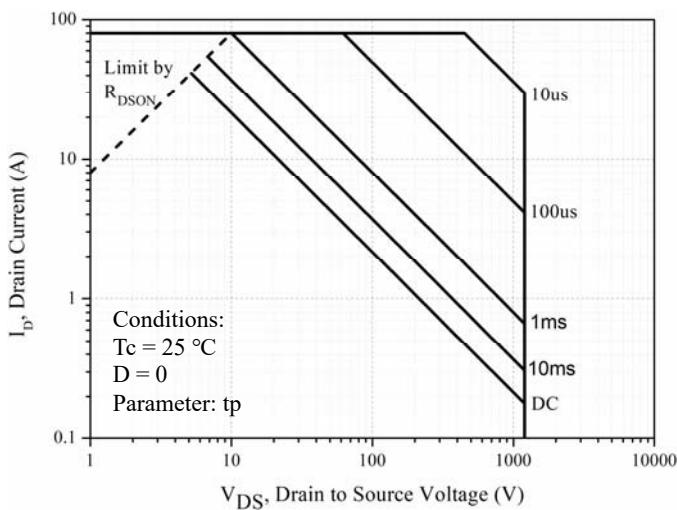
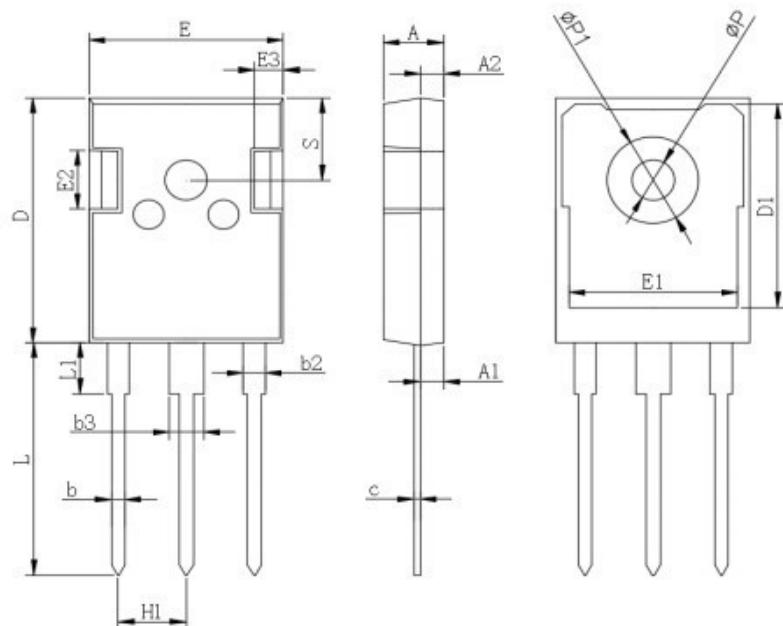


Figure 23. Drain to Source Voltage (V)

## ■Outline Dimensions



TO-247AB		
Dim	Min	Max
A	4.8	5.2
A1	2.21	2.61
A2	1.85	2.15
b	1	1.4
b2	1.91	2.21
b3	2.8	3.2
c	0.5	0.7
D	20.7	21.3
D1	16.25	16.85
E	15.5	16.1
E1	13	13.6
E2	4.8	5.2
E3	2.3	2.7
L	19.62	20.22
L1		4.3
ΦP	3.4	3.8
ΦP1		7.3
S	6.15 Typ	
H1	5.44 Typ	



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